

# Experimental Assessment of the Battery Lifetime in WSN Based on the *Duty-Cycle Current Average* Method

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#### **Abstract**

A great amount of work addressed methods for predicting the battery lifetime in wireless sensor systems. In spite of these efforts, the reported experimental results demonstrate that the *duty-cycle current average* method, which is widely used to this aim, fails in accurately estimating the battery life time of most of the presented wireless sensor system applications. The aim of this paper is to experimentally assess the *duty-cycle current average* method in order to give more effective insight on the effectiveness of the method. An electronic metering system, based on a dedicated PCB, has been designed and developed to experimentally measure node current consumption profiles and charge extracted from the battery in two selected case studies. A battery lifetime measurement (during 30 days) has been carried out. Experimental results have been assessed and compared with estimations given by using the *duty-cycle current average* method. Based on the measurement results, we show that the assumptions on which the method is based do not hold in real operating cases. The rationality of the *duty-cycle current average* method needs reconsidering.

## Kevwords

Battery Lifetime, Current Consumption, TinyOS, Measurement, Duty-Cycle Current Average Method

## 1. Introduction

Nowadays, WSNs are used for many different applications, but systems are designed in a rather ad-hoc manner without unified and systematic battery lifetime analysis. Since the quantity of battery charge is limited, it is desirably the longest possible lifetime.

For instance, in [1] authors present an ad-hoc WSN for motor energy monitoring but the node lifetime estimation method is not discussed. In [2] authors discuss WSN design for industrial automation applications; technical aspects and design approach are remarked, but issues related to node lifetime estimation are not addressed. In [3] authors introduce a solar energy self-powered wireless sensor network. Issues regarding the devices for energy storing are discussed but the node current consumption is not modeled. In [4], authors highlight the need of achieving reduced current consumption in order to maximize battery lifetime but a systematic methodology for achieving is not proposed. In [5] a WSN application for pedestrian tracking and localization is presented, but the node lifetime estimation is not addressed. On the other hand, nowadays WSNs are also used as part of precision agriculture tools [6] [7], but methods related to node lifetime estimation still remain omitted. To the extent of our knowledge, after reviewing the state of the art (by means of well-known bibliographic and citation database of peer-reviewed scientific literature like SCOPUS, EBSCO, JSTOR, IOP science, and IEEE Xplore), we argue that WSN designers avoided carrying out long-term measurements in order to validate battery depletion estimate models for WSN applications. Designers use the *duty-cycle current average* method, which is nowadays the most accepted method. However, designers often report that batteries are depleted prematurely with respect to the estimation given by such method (e.g. [8]-[12]).

The aim of this paper is to experimentally assess the *duty-cycle current average* method in order to give more effective insight on the effectiveness of the method. We present a counterexample based on measurements in order to show how the *duty-cycle current* average method can fail the battery lifetime estimation. As we need a solid understanding of the battery current consumption profile, we have designed an electronic metering system based on a dedicated PCB, which visualizes the node current consumption profile and charge extracted from the battery during the wireless sensor node operation (e.g. transmission, signal sampling). The node used in the experiments is the MICAz [13] running applications developed by using nesC language [14]. The TinyOS [15] version is provided by the software platform MOTEWORK [16] and the radio transceiver is the Chipcon CC2420 [17]. The node is powered by two non-rechargeable alkaline IEC-LR6 cells, size AA.

By using the proposed electronic metering system, measurements of two case studies have been performed; the experimental results provide evidence to explain why the *duty-cycle current average* method is not reliable for the estimation of the battery lifetime. An experimental battery full-depletion time measurement has been conducted to compare the result against the estimation given by the *duty-cycle current average* method.

## 2. Wireless Sensor Task and Duty-Cycle Definition

The TinyOs operating system manages the node hardware resources; it is based on the hardware abstraction layers software model (TEP 2 [15]), where each level gives services to upper layers through interfaces. The bottom layer is named Hardware Presentation Layer (HPL) and it is responsible for accessing hardware resources. This is explained in the documented TinyOS Enhancement Proposals also named TEPs (in this case see TEP 2 from [15]). By using this software model, designers can develop programs by using customized interfaces through which the lower hardware abstraction layers are invisible. The WSN application is implemented at the top level (Hardware Interface Layer or HIL). In this framework, the tasks represent applications performed by the wireless sensor node: e.g. sensor signal sampling, radio transmission and signal processing. The node wakes up (from power down state) in order to execute the task and afterwards goes into sleep mode (power down). In other words, a task is the activity that the node performs between two power down states. Each task is executed at the rate R (number of tasks per second) and the task time duration is defined as the active period  $T_A$  (each task has its own  $T_A$ ). The node duty-cycle time interval D is defined as  $D = RT_A$ . During the execution of a task, the node circuit consumes the battery charge  $Q_c = \int_0^{T_A} i(t) dt$  which is named task charge cost henceforth. Figure 1 shows the current consumption profile for a transmission task, where the marked time intervals are: interval A  $(T_1)$  node wake up; interval B  $(T_2)$ , radio transceiver start-up; interval C  $(T_3)$ , MAC layer functions such as channel assessment and backoff-time calculations and receiver system calibration; interval D  $(T_5)$ , transmission; interval E  $(T_7)$ , reception mode according to the MAC protocol [17], waiting for ACK packets. At the end, the node gets power down state (P.D.), waiting for the next timer interruption for next

## 3. Battery Lifetime Estimation Approach

task execution.

As Figure 2 shows, we need to estimate the time  $L_s$  (node lifetime or service hours) at which the battery vol-

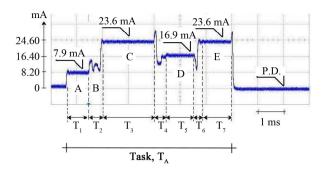
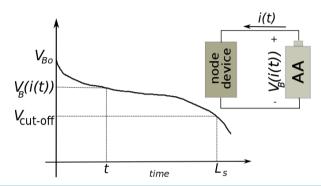


Figure 1. Battery current profile at -25 dBm output power. Time values are:  $T_1 = 0.84$  ms,  $T_2 = 0.58$  ms,  $T_3 = 2.00$  ms,  $T_4 = 0.49$  ms,  $T_5 = 1.1$  ms,  $T_6 = 0.36$  ms,  $T_7 = 1.07$  ms. The sum is the activity time (or task time)  $T_4 = T_1 + T_2 + T_3 + T_4 + T_5 + T_6 + T_7 = 6.44$  ms.



**Figure 2.** Battery voltage profile;  $V_B(i(t))$  is the battery voltage at time t;  $V_{Bo}$  is the initial battery voltage,  $L_S$  is the node lifetime (service hours) and i(t) is the battery current.

tage  $V_B$  drops below the cut-off voltage  $V_{\text{cutt-off}}$ . To estimate the  $L_S$  value, the node current consumption profile i(t) and the equation that relates i(t) to the battery voltage, *i.e.* the equation  $V_B(i(t))$  are needed. The current consumption i(t) is determined by the hardware platform and the operating system features.

There has been a lot of effort for building models capable of predicting the current consumption profile i(t), see for instance [18]-[21], but the problem is difficult and not easily tractable.

On the other hand, see for instance [22]-[25], there is no consensus about the description of the battery voltage  $V_B(t)$  as function of i(t). Approaches have converged toward the *duty-cycle current average* method, which is the most widely used to estimate the  $L_s$  value. The method approximates the node current consumption with step-shaped waveforms (named  $i_s(t)$  from herein) that best approximate i(t). The node duty-cycle is assumed to be deterministic and known beforehand. The  $i_s(t)$  integration over the time  $T_A$  estimates the node charge consumption value. This charge value is then used to estimate the average constant current consumption  $i_d$  for such duty-cycle; it is used to estimate the lifetime  $T_s$  value by means of the graph *constant current discharge vs. hours service*, as **Figure 3** shows, which is provided by the battery datasheet. From the battery service hours graph it is possible to derive the log-linear relation between the constant current  $i_d$  and the service hours  $T_s$  for a given cutoff voltage (e.g. the marked ones with 0.8 V, 1 V and 1.2 V in **Figure 3**). For example, for the cutoff voltage value 1.2 V, the straight line linking the points (20 mA, 100 h) and (80 mA, 20 h) is as follows:

$$\log_{10}(T_s) - \log_{10}(20) = m(\log_{10}(i_d) - \log_{10}(80))$$
(1)

The coefficient m is given by:

$$m = \frac{\log_{10}(100) - \log_{10}(20)}{\log_{10}(20) - \log_{10}(80)} = -1.2$$
 (2)

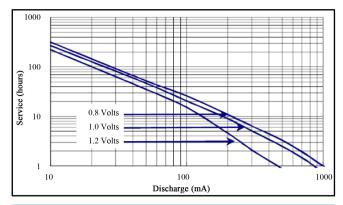


Figure 3. Battery service hours for different constant current consumption values (figure from [29]).

Then:

$$\log_{10}(T_s) = m\log_{10}(i_d) - m\log_{10}(80) + \log_{10}(20)$$
(3)

$$\log_{10}(T_s) = -1.2\log_{10}(i_d) + 3.51 \tag{4}$$

Some node platforms vendors provide electronic sheets based on this method; see for example the *Mote Battery Life Calculator from Crossbow Technology* [26] and other examples given by [27] [28]. For examples, in Equation (4) if: 1)  $i_d = 2.04$  mA and 2)  $i_d = 3.02$  mA then respectively: a)  $T_s = 1375.4$  h (i.e. 57.3 days) and b)  $T_s = 859.0$  h (i.e. 35.8 days).

To summarize, the hypotheses for using the duty-cycle current average method are the following:

- $H_1$ ) the node current consumption profile as function of the given application is known. It is a deterministic waveform that can be always approximated by a step-shaped waveform. The node duty-cycle will be constant during the node lifetime.
- $H_2$ ) superposition principle: let's assume that a node performs task 1 with charge cost  $Q_{c1}$  during lifetime  $L_{s1}$ . On another hand, let's assume that the same node performs task 2 with charge cost  $Q_{c2}$  during lifetime  $L_{s2}$ . If task 3 is defined as the task 1 plus task 2 (e.g. task 1 performs transmission and task 2 performs signal sampling and data storage, then, task 3 implements both activities, signal sampling, data storage and transmission), the superposition principle states that  $Q_{c3} = Q_{c1} + Q_{c2}$ .

We aim to demonstrate that  $H_1$ ) and  $H_2$ ) hypotheses do not always hold. In the next section, we present the metering system that has been used to experimentally assess the two hypotheses.

# 4. Electronic Metering System

The metering system is based on a dedicated PCB: it visualizes the node current consumption and the task's charge cost  $Q_c$ . The prototype PCB is based on the following devices: the High Side Current Shunt Monitor INA139 [30] and the Coulomb Counter LTC4150 [31]. Figure 4 shows the metering system block diagram. The INA139 amplifies the voltage across a shunt resistance between battery and load. The measured voltage is proportional to the battery current consumption. The output voltage signal is in input to an amplification stage and subsequently to an oscilloscope where the current waveform associated to the node task can be analyzed. The shunt resistance value has been selected so that the INA139 gain provides the maximum output voltage range at the maximum node current consumption. The board is powered by a 3 V voltage. The charge counter characterizes the node in terms of consumed charge; values are expressed in charge units (mAh or  $\mu$ Ah). The LTC4150 device is a voltage integrator with a voltage-to-frequency converter. The coulomb counter device forces to zero the output pin voltage when a fixed quantity of charge  $Q_M$  has been measured.

The value of  $Q_M = 5.2 \,\mu\text{Ah}$  has been obtained by calibration as it is suggested in the device datasheet. The time period between consecutive output pulses is proportional to the value of  $Q_M$ . Figure 4 shows the block diagram of the experimental setup. If the number of counted pulses within a given time observation window T is equal to K, then the total charge consumed by the node is  $Q_T = K \cdot Q_M$ . Under the assumption that the node

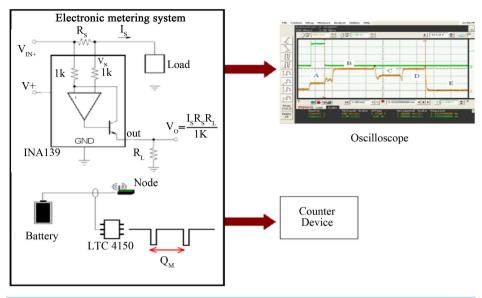


Figure 4. Block diagram of the experimental setup.

executes N times the same task during the time T, we estimate the average task charge cost value  $Q_c$  as Equation (5), where N is such that  $N = R \cdot T$  (the measurement ends when time T is reached and the task has been executed N times).

$$Q_c = \frac{Q_T}{N} = \frac{KQ_M}{N} \tag{5}$$

The error is given by Equation (6) where  $\Delta K = 1$ 

$$\Delta Q_c = \frac{\Delta K \cdot Q_M}{N} \tag{6}$$

We use the following example to illustrate the methodology. If the node performs packet transmissions at the rate R = 10 packets/s, for a given time, e.g. T = 3600 s, the number of performed tasks (equal to the number of packet transmissions) is N = 36000. If we count, for instance, K = 1000 pulses, from Equation (5) the charge cost associated to the transmission task and the related error result as follows:

$$Q_c = 1000 \times \frac{5.2 \,\mu\text{Ah}}{36000} = 144.4 \,\text{uAh} \tag{7}$$

$$\Delta Q_c = \frac{5.2 \text{ } \mu\text{Ah}}{36000} = 144.4 \text{ pAh} \approx 0.15 \text{ nAh}$$
 (8)

As a final remark, we observe that the best set of values  $\{T,R\}$  to perform the experiment is such that  $N\gg 1$  values (e.g. 1000 or more), in order to meet the condition  $\Delta Q_c\ll Q_c$ . The proposed method has the disadvantage that it requires long time measurements. A suitable value of T (or N) is not known a priori, because the value  $Q_c$  is obviously not known, as well as the K value. This problem can be overcome with an iterative procedure, by performing several measurements. In general from our experience, we have not conducted measurement of more than 4 hours. In this article, the node tasks under evaluation are transmissions (task 1) and temperature sensor sampling (task 2). The measured amount of charge  $Q_c$  for each task is shown in Table 1.

#### 5. Battery Lifetime Time Measurement

The MICAz node has been programmed to transmit one packet with a payload of 25 bytes, at the output power level of -25 dB·m. The transmission rate is R = 20 s<sup>-1</sup> (*i.e.* 50 ms time interval between packet transmissions). Please note that the given transmission rate value is high for usual real wireless sensor networks applications.

**Table 1.** Measured charge  $Q_{c}$ 

Task	Charge cost $Q_c$ [ $\mu$ Ah]
Task 1	0.042
Task 2	0.520

Power voltage supply: 3 V; Output power: -25 dB·m·\$, payload size 25 bytes.

The transmission rate value has been selected to perform the experiment in a reasonable time (*i.e.* some days, no more than one month). The node could be considered not working when the voltage power supply drops value below 2.4 V (*i.e.* individual cell voltage value of 1.2 V). We daily measured the battery voltage and the result is shown in **Figure 5**; the voltage value 2.4 V has been achieved in 30 days.

The current consumption profile has been measured during transmission; it is shown in **Figure 1**. The resulting duty-cycle is D = 6.44 ms/50 ms = 0.13. In accordance with the *duty-cycle current average* method, it is assumed that this current profile is repeated each time that the radio performs a transmission. By numerical integration, the average discharge current inside the duty-cycle time interval is given by  $i_d = 2.04$  mA; the estimated battery lifetime (Equation (4)) is  $T_s = 1375.4$  hs, *i.e.* 57.3 days, with an error of 27 days with respect to the measured one. At this point, it is necessary to understand why there is such big difference between estimated and measured results. To this aim, we used the metering system for visualizing the node current consumption profile.

# 6. Assessment of $H_1$ Hypothesis

Differently to the previously presented lifetime estimate, we use  $Q_c = 0.042~\mu Ah$  (see **Table 1**). The average current consumption is  $i_d = Q_c/50~ms = 3.02~mA$  and by means of Equation (4), the estimated lifetime is  $T_s = 859.0~h$  (i.e. 35.8 days), which is a better approximation with respect to the previous one. We conclude that the  $H_1$  hypothesis does not hold. The metering system provides excellent accuracy to measure  $Q_c$  for large N values. It is not necessary to assume any node current consumption profile beforehand and the overall current consumption is taken into account to measure  $Q_c$  (including node start-up and power down states). The reduced current consumption levels are frequently neglected in the duty-cycle current average method, nevertheless, they could have cumulative effects during the node lifetime. It is not enough to observe the current consumption profile for a short time to estimate the real required charge  $Q_c$  because our measurements shows non periodic current consumption profile. In fact, we observed a time-varying duty cycle (time-varying current consumption). **Figure 6** shows three different observed cases, among others. The difference between duty-cycle values of cases A and C is 48.9%. Then, it is not possible to have a deterministic known current profile during transmissions and the  $H_1$  hypothesis which states that the duty-cycle is constant, does not hold.

Therefore, without a reliable value of the duty cycle value, the *duty-cycle current average* method can estimate the lifetime value very roughly. In fact, it is not possible to assume a priori node current consumption profile and the experimental characterization should be done for every given application. The node tasks are controlled by the program that runs inside the microcontroller under the supervision of an operating system like TinyOS [14]. Most of the time, the operating system asynchronously manages hardware resources in order to provide services to asynchronous events related to the wireless communication. More specifically, the origin of this non deterministic behavior is given by the method used for the clear channel assessment in most of the MAC layer protocols (see *Chapter 5 MAC Protocols* in [20]). Therefore, generally speaking, the node awakes and sleeps in a non-deterministic way and the battery is forced to deliver current patterns which are not easily predictable, neither the duty-cycle value. The *duty-cycle current average* method requires the knowledge of the deterministic node current consumption, but we assessed that it is not possible.

# 7. Assessment of $H_2$ Hypothesis

**Table 1** shows charge costs for the tasks 1 and 2. Task 3 is defined as the sampling plus the transmission tasks, *i.e.* the node performs signal sampling and then packet transmission. The  $H_2$  hypothesis states that the expected battery charge cost  $Q_{c3}$  for task 3 should be equal to the sum of  $Q_{c1}$  (task 1) and  $Q_{c2}$  (task 2). To verify the  $H_2$  hypothesis, we implemented the task 3 and experimental measurements have been performed.

The measurements show that  $H_2$  hypothesis does not hold. The results are presented in **Table 2**; they are sig-

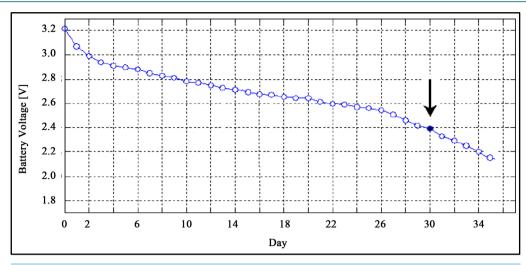


Figure 5. Measured battery voltage during node transmissions

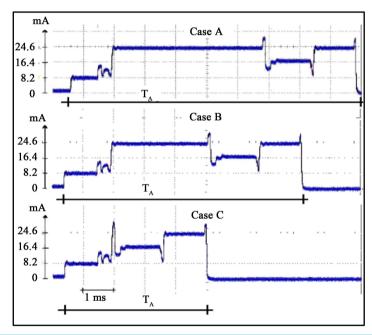


Figure 6. Three different measurements that show the time-varying node current profile behavior because of the random nature of backoff-time calculation for the MAC layer. Please, note how the  $T_A$  value varies. Case A:  $D \approx 9.2$  ms/50 ms or 18.4%; Case B:  $D \approx 7.5$  ms/50 ms or 15.0%; Case C:  $D \approx 4.5$  ms/50 ms or 9.0%.

**Table 2.** Measured charge  $Q_c$ .

Task	Charge cost $Q_c$ [ $\mu$ Ah]
Task 1 + Task 2	0.520 + 0.042 = 0.560
Task 3	2.04

nificantly different with respect to the expected ones.

The reason behind this result is due to the effects of node programming methods and compilers features which have a strong influence on the node current consumption profile. In fact, the operating system behavior and how hardware resources are managed, make unreliable the application of the superposition principle.

No withstanding the fact that programs are written in nesC, *i.e.* they are based on customized interfaces and modules which provide modularity, encapsulation and re-usability features; such benefits are counterbalanced by the impossibility of using the *duty-cycle current average* method for battery lifetime estimation.

#### 8. Conclusions

The WSNs are comprised of battery-powered embedded electronic devices. A great amount of research has been devoted to the development of methods capable of predicting battery lifetime. Regardless of such efforts, most of the wireless sensors applications have prematurely depleted batteries, contrary to the estimations based on the widely adopted *duty-cycle current average* method. Our work experimentally assesses such discrepancy. An electronic metering system, based on a dedicated PCB to experimentally measure node current consumption profiles and charge extracted from the battery for two selected case studies, has been designed and implemented. Based on the measurement results, we demonstrate that the hypotheses on which the method is based are not reliable. The concluding remarks to be taken into account for the development of WSN applications are: 1) the software architecture and how to develop the application program have strong influence on the node current consumption profile and hence, on the battery lifetime; 2) the node current consumption profile as function of the given application as well as the duty-cycle is not deterministic. The *duty-cycle current average* method has another hypothesis which has not been considered: the battery operating temperature, which would have significant effect on the graph *constant current discharge vs. hours service*. It should be addressed as future research trend.

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