

The Research on the Quick Discharge Circuit of Shunt Capacitor

Shuheng Dan, Lei Ning

Department of Electrical Engineering, Shanghai University of Electric Power, Shanghai, China
Email: danshuheng@shiep.edu.cn

Received November 2013

Abstract

Shunt capacitor is the main method to intensively compensate reactive power in the power system. The external discharge circuit, commonly discharge coil which is an inductance coil with low resistance, must be allocated to the large capacity of capacitor. This paper calculates and compares two kinds of circuit, capacitor placed with a discharge coil or a resistance, to find the best way to discharge the residual charge of capacitor. Through the study, in the under damped case, these two kinds of circuit present different discharge properties. In over damped mode, their discharge properties are similar.

Keywords

Discharge Coil; Discharge Property; Capacitor

1. Introduction

In the 66 kV and below power system, discharge coil shunted with high-voltage capacitors quickly release the residual charge of the capacitor as it removed from the power system to ensure the security of the capacitor itself and other devices when they are put into use again [1]. The whole process of discharge can be equaled with RLC circuit in which charged capacitance C interact with inductance L and resistor R [2]. The combination of the three parameters may result in oscillation which should be avoided in actual operation [3]. In the process of discharge, the RC circuit without inductance L can not only prevent the phenomenon of oscillation, but also reach the purpose of releasing energy. This paper compared the discharge property of the two circuits by observing time t, during which the voltage of capacitor decays to a certain value, and the variation of the current.

2. The Mathematical Model

The power industry standard DL/T653-2009-HV shunt capacitor discharge coil ordering technology [4] makes a clear regulation of the discharge property of discharge coil. The voltage of the capacitor removed from the power system drops from $\sqrt{2}U_n$ to 50 V in 5 s.

After the RLC equivalent circuit switch closed, the whole process is equal to zero input response of the second-order circuit. In order to make sure the changing circumstance of the voltage U_c which is between the two ends of the capacitor, the second order constant coefficient differential equation with its initial conditions of

the equivalent circuit depicted in **Figure 1** should be given based on the knowledge of circuit, like in Equation (1).

$$\begin{aligned} LC \frac{d^2 U_c}{dt^2} + RC \frac{dU_c}{dt} + U_c &= 0 \\ U_c(0_+) &= U_0 \\ \left. \frac{dU_c}{dt} \right|_{t=0_+} &= 0 \end{aligned} \quad (1)$$

Due to the relation between the circuit resistance R and the critical resistance $R_{cr} = 2\sqrt{L/C}$, the situation of U_c can be divided into four kinds of circumstance, over damped non-oscillation, Critically damped non-oscillation, under damped oscillation and undamped oscillation.

Because the remaining energy cannot be consumed the undamped oscillation circuit is not satisfied with the demand of the standard. The other three kinds of solution are described in the following Equations (2)-(4).

$$U_c(t) = U_0(1 + \delta t)e^{-\delta t} \quad (2)$$

$$U_c(t) = \frac{U_0}{s_2 - s_1} (s_2 e^{s_1 t} - s_1 e^{s_2 t}) \quad (3)$$

$$U_c(t) = \frac{\omega_0 U_0}{\omega} e^{-\delta t} \sin(\omega t + \beta) \quad (4)$$

where,

$$\begin{aligned} \delta &= R/(2L) \\ s_{1,2} &= -\delta \pm j\omega \\ \omega_0 &= \sqrt{1/(LC)} \\ \omega &= \sqrt{1/(LC) - (R/(2L))^2} \end{aligned}$$

It is clear that if the Equation (4) wants to meet the need of the standard, the coefficient, which is $(\omega_0 U_0) \cdot e^{-\delta t} / \omega$, should decays to 50 V in time. Equation (5) is a simplified formula.

$$t_{RLC} = \frac{2L}{R} \ln \left(\frac{U_0}{50} \cdot \frac{1}{\sqrt{1 - \frac{R^2 C}{4L}}} \right) \quad (5)$$

Figure 2 shows a RC circuit. After the switch closed, the capacitor C interacts with the resistance R. The whole process is equal to zero input response of the first-order circuit.

The first order constant coefficient differential equation and the initial conditions of the RC equivalent circuit are given in Equation (6).

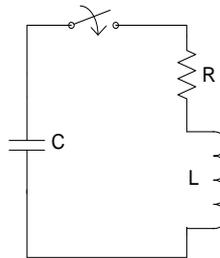


Figure 1. RLC equivalent circuit.

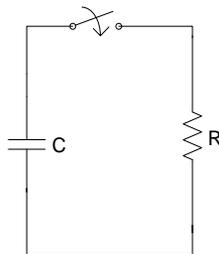


Figure 2. RC equivalent circuit.

$$RC \frac{dU_c}{dt} + U_c = 0 \quad (6)$$

$$U_c(0_+) = U_0$$

And the solution is Equation (7)

$$U_c(t) = U_0 e^{-\frac{t}{RC}} \quad (7)$$

Letting the right side of Equation (7) equals to 50, the time t in which the voltage of the capacitor drops to 50 V can be got in Equation (8).

$$t_{RC} = RC \ln \frac{U_0}{50} \quad (8)$$

3. The Analysis and Calculation of the Mathematical Model

The type of capacitor is BW10.5-12-1 and its capacitance is $0.347 \mu F$. The reductive reactance in circuit is 12% of the capacitive reactance. Because of the uncertainty of the solution of the second order constant coefficient differential equation the comparison of the discharge property of the two kinds of circuit must be discussed separately.

The critical resistance R_{cr} equaled to $2\sqrt{L/C}$ in RLC circuit is determined by capacitance C and inductance L . Assuming that the circuit resistance R is a variable, the situation in RLC circuit can be analyzed in the situations of over damping, under damping and critical damping based on the relation between the circuit resistance and the critical resistance. In RC circuit, the size of resistance R can also be classified according to this standard.

3.1. Critically Damped Case

In this case, the circuit resistance of the RLC and RC circuit is both equal to critical resistance. Equations (2) and (7) are both monotonous attenuation functions. In order to get their discharge property, the curves of the voltage of the capacitor can be drawn separately in **Figure 3**.

Figure 3 shows that at the beginning the discharge rate of RC circuit is higher than RLC circuit, nevertheless the situation then changes as the voltage decays to a certain value. Compared with the RC circuit, the voltage of the RLC circuit uses less time to reach the established value.

3.2. Under Damped Case

The resistance of the two circuits here is smaller than critical resistance. **Figure 4**, in which the resistance R is a variable, shows the curves of voltage-decaying in under damped state. Time t_{RC} which stands for the duration of voltage-decaying in RC circuit increases as the resistance increases. But for the RLC circuit, time t_{RLC} decreases at first and then increase as the resistance increases. Considering this mathematical model, time t_{RLC} is significantly greater than the time t_{RC} as the resistance changes in the range of a few hundred ohms. With the value of resistance keeping increasing and reaching $0.731R_{cr}$ (4647Ω) which is the maximum of the range, time t_{RLC} is greater than time t_{RC} all the time, but the gap is constantly narrowed. When the circuit resistance

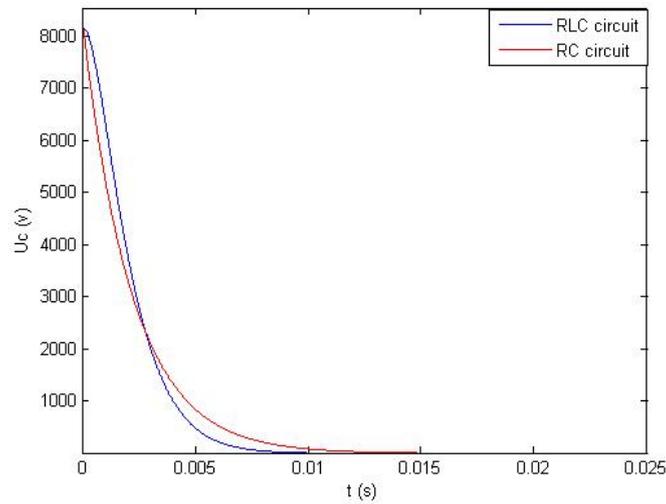


Figure 3. Voltage-decaying in critically damped case.

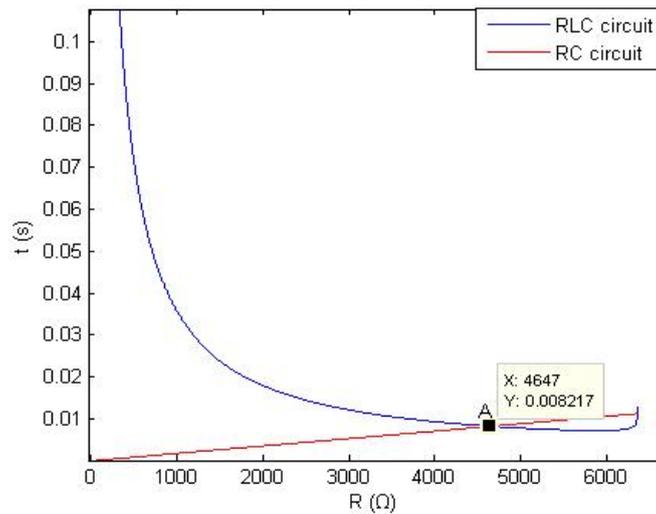


Figure 4. Time for voltage-decaying to 50V in under damped case.

changes in the range $(0.731R_{cr}, R_{cr})$, time t_{RLC} is smaller than time t_{RC} . In addition, these two curves have a tendency to intersect at the critical resistance.

3.3. Over Damped Case

The resistance of the two kinds of circuit is larger than critical resistance. Equation (3) established the voltage-decaying of the RLC circuit in over damped non-oscillation. Given by the equation, U_c is a superposition of two items of exponential decay and the capacitor is discharging all the time.

Figure 5 is the curves of Equations (3) and (8) and the relation between the voltage-decaying time and the variable R. From this figure, the time t increases as the circuit resistance increases and the two lines are less distinct.

The solutions of Equations (1) and (6) can be calculated by MATLAB. Adjusting the size of the resistance in the circuits the series of voltage-decaying time can be obtained in Table 1. And the first column is the ratio of circuit resistance and critical resistance.

In Table 1, in RLC circuit time t decreases at first and then increases as the circuit resistance increases consistently. However, time t in RC circuit is increases all through. Compared with the two columns of Table 1, time t is similar when the circuit resistance is above R_{cr} . Take $5R_{cr}$ as an example depicted in Figure 6.

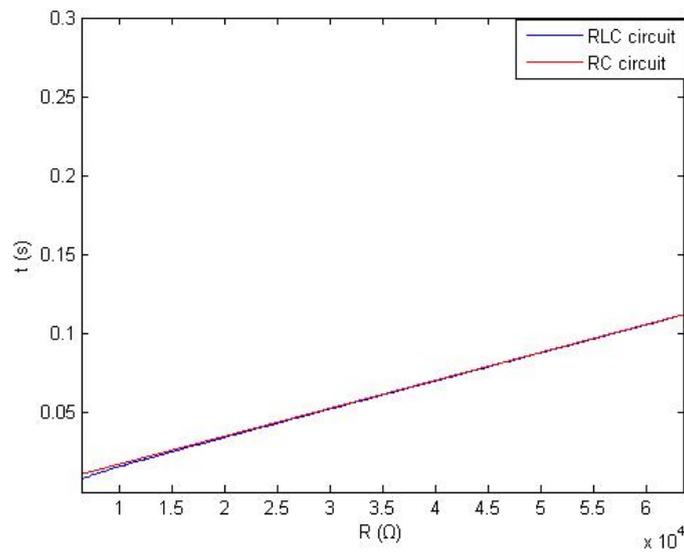


Figure 5. Time for voltage-decaying to 50V in over damped case.

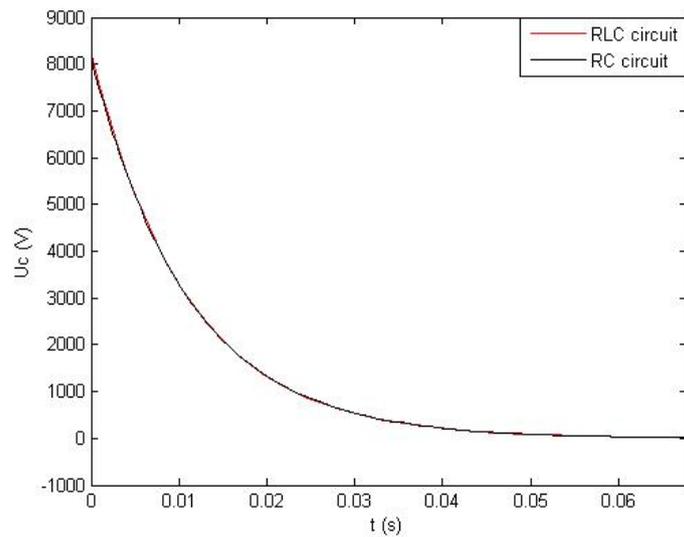


Figure 6. Voltage-decaying in $5 R_{cr}$.

Table 1. Time for voltage-decaying to 50 V with variable circuit resistance.

Ratio R / R_{cr}	Time for voltage decaying to 50 V t/ms	
	RLC circuit	RC circuit
5	54.84	57.08
2	21.5	22.83
1	8.03	11.42
0.9	5.9	10.27
0.7	7.7	7.99
0.5	10.04	5.71
0.1	25.9	1.14

3.4. The Change of Circuit Current

Figure 7 is a variation of circuit current when the circuit resistance equals to $0.731R_{cr}$. The amplitude of the current in RC circuit is higher, which is about 1.6 times of RLC circuit's value, meanwhile the duration of voltage-decaying to 50 V of both circuits is same.

It is a common sense that excess current may influences the normal operation, or even destroys devices. **Figure 8**, in which x-coordinate is the ratio of circuit resistance and critical resistance, shows the relation between the current peak and circuit resistance. The amplitude of both circuits is decreases as the circuit resistance increases. And the peak value of RLC is smaller than RC's.

4. Conclusions

This paper analyzes the property of a capacitor assembled with a discharge coil and a resistance. And some general conclusions can be obtained based on these results.

1) In under damped case, in RLC circuit the voltage-decaying time decreases at first and then increases as the circuit resistance increases; in RC circuit the time increases consistently. In over damped case, the trend of the voltage-decaying in two circuits is similar.

2) The amplitude of the current in both circuits goes down as the resistance increases, and the peak value of current in RLC circuit is smaller than RC's all the time.

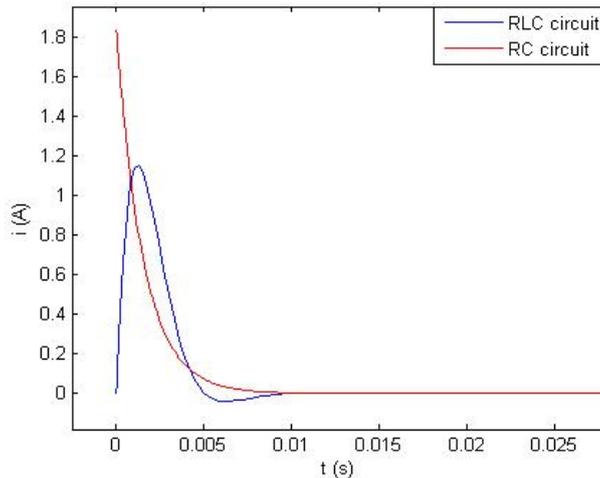


Figure 7. Current in two circuits with R equaled to $0.731 R_{cr}$.

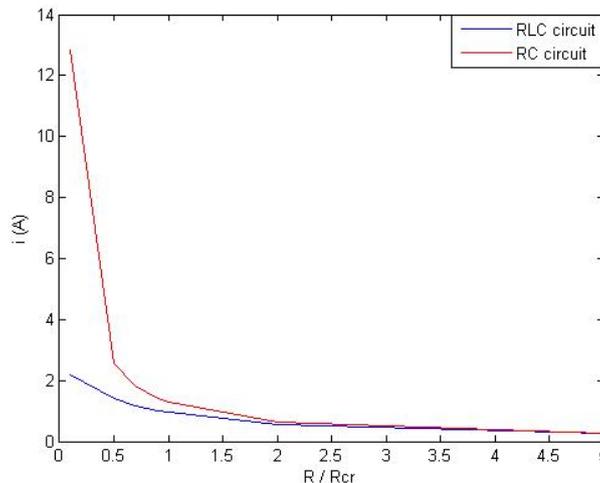


Figure 8. The current peak with the variable ratio R / R_{cr} .

3) Considering of actual operation, discharge circuit should limit the size of discharge time and the current peak well. On this occasion defined by minimum discharge time and smaller current peak, the property of discharge coil is better than resistance.

Acknowledgements

I owe appreciation to the fund of national nature science and the Shanghai Municipal Education Commission. This paper is financially supported by the Fund of National Nature Science, project number is 50577040. And supported by Leading Academic Discipline Project of Shanghai Municipal Education Commission, project number is J51303.

References

- [1] Zhang, H.L., Huang, X.M. and Wu, Y.M. (2010) Selection of Discharge Coil for High Voltage Shunt Capacitor Bank. *Power Capacitor and Reactive Power Compensation*, **31**, 26-29.
- [2] Zhou, G.L. (2000) Test and Research on Characteristics of Discharge Coils for High Voltage Shunt Capacitors. *Zhejiang Electric Power*, **5**, 7-10.
- [3] Wu, B. and Wang, X.J. (2006) Research on the Parameters of Discharge Coil of Shunt Capacitor Bank. *Tianjin Electric Power Technology*, **4**, 35-37.
- [4] The National Energy Administration of China (2009) DL/T653-2009 HV Shunt Capacitor Discharge Coil Ordering Technology. China Electric Power Express, Beijing.