

New 3D Aware Formulation of MoM-GEC Method for Studying Planar Structures with Vertical Sources

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Abstract

This paper proposes a generalization of the MoM-GEC method [1] needed for studying planar structures excited with a source located at perpendicular plan relative to circuit plan. A general formulation is detailed to allow modeling excitation of a planar structure with one or more sources located in plans other than the circuit plan. The numerical approach elaborated is based on the definition of new admittance operators and rotational transformations describing the transition from one plan to another. To validate this approach, we consider the case of a single source located in the perpendicular plan to the circuit.

Keywords

Admittance Operator, MoM-GEC 3D, Discontinuities, Vertical Source

1. Introduction

Studying of microwave planar circuits is based on EM characterization of the structure via determination of electrical (E) and magnetic (H) fields and the electric current density J using Maxwell equations. To solve these equations, iterative methods are used (FDTD [2] [3] and FEM [4] [5]) providing accurate results but requiring a relatively heavy calculation time [6]. To overcome this disadvantage, integral methods have been proposed. Integral equations introduce an excitation term in the integral formulation of an electromagnetic problem that needs to establish an appropriate mathematical model: source model [7].

As the source is the knowledge of an electromagnetic field distribution on a circuit surface independently of the load, one distinguishes two source models: localized source modal [8] [9] and extended source modal [6] [7]

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defined in the perpendicular plan to the circuit. The source can be considered as a discontinuity causing the creation of higher order modes at the border source/circuit [10]-[15]. This discontinuity can be corrected by using a coupling quadripole [8] [16]-[18]. That generates an additional computing time. Considering the fundamental mode of the access line can be an optimized solution which allows us to have a perfect adaptation between source and circuit [19] [20].

Localized source remains a theoretical and relatively simple model to describe studied structure. Such model, is not fully consistent with actual excitations located in another plan than the circuit which favors the extended source, hence the importance of our study.

Our goal is to develop an exact source method based on MoM-GEC method for studying planar structures excited by sources located in any other plan. We first establish a general formulation for the case of N sources. We verify, then the accuracy of the hypothesis of sources simplification: localized planar sources.

3D extension of MoM-GEC method has not been really focused on in previous work. Related work mainly includes Hamdi *et al.* work in [13]. However, this work focused on input impedance and scattering matrix evaluation rather than a full EM characterization. In our study, we define new admittance operators to bind excitation sources to electrical quantities defined in the circuit plan and thus help calculate EM fields accurately. We also introduce a new rotational transformation describing the transition from one plan to another. The determination of these operators is important because it allows us to perform three-dimensional calculation while keeping homogeneous the MoM-GEC method (decomposition of operators on the TE and TM modes).

In this paper, input impedance, current density and electric field distribution are evaluated and discussed in the case of a single vertical source. Results are compared to commercial software HFSS and CST. A couple of structures are studied: microstrip short-circuited line and microstrip open circuit line as an application.

This paper is organized as follows: we start presenting the new approach in the case of N sources by exposing the general formulation of integral equations to determine the admittance matrix. Then, we detail the formulation and the determination of input admittance (y_{in}) in the case of a single extended source located at the perpendicular plan to circuit. In Section 4, we focus on determining admittance operators. Finally, in Section 5 we present different simulations results obtained for the two studied structures.

2. New Formulation of MoM-GEC Method for 3D Structures

In this section, we introduce and explain the general formulation for 3D structures by considering the case of N-ports (N sources) located at perpendicular plans to the circuit. **Figure 1** shows the general structure excited by

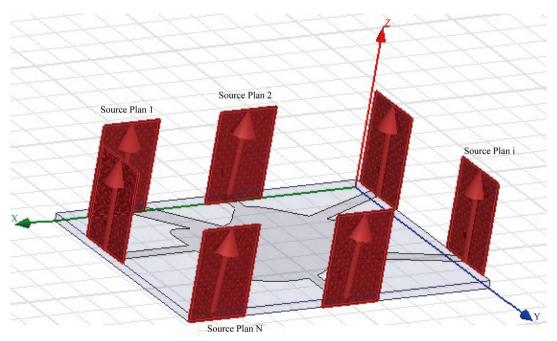


Figure 1. Planar structure with *N* discontinuities (*N* sources)

voltage sources ($E_{01}, E_{02}, \dots, E_{0N}$). The electromagnetic analysis of this structure consists in solving an integral equation expressing the boundary conditions of electromagnetic fields of sources and circuit plans. This equation has the following form:

$$L(f) = g. (1)$$

where L is an integro-differential operator, g is the excitation source and f is the unknown to be determined.

In this work, L is an admittance operator, f is the electric field E tangential to the circuit plan and g represents the excitation sources. The method consists in solving this equation by the Galerkin method (a variety of the MoM method) to determine the electric field E and deduce the input admittance Y_{in} matrix

The circuit is printed on a dielectric substrate of relative dielectric constant ε_r with a thickness h. N transmission lines feed the structure defining N discontinuities. Each transmission line is excited with a vertical port. Discontinuities at port level is overcome by considering fundamental mode of the transmission line.

To characterize this discontinuity, we used the formalism of admittance operator and assume that excitation sources are totally independent and completely decoupled (electromagnetic coupling) for each other [21] [22]. It would be necessary to determine with precision the fundamental mode of the attachment lines to ensure good adaptation.

The electromagnetic quantities in source plans (from P_1 to P_N) and planar circuit (c) are expressed in following relations (Equation (2)):

$$\begin{cases}
\left|J_{01}\right\rangle = \left|\hat{Y}_{11}E_{01}\right\rangle + \dots + \left|\hat{Y}_{1N}E_{0N}\right\rangle + \left|\hat{Y}_{1,c}E\right\rangle \\
\vdots \\
\left|J_{0i}\right\rangle = \left|\hat{Y}_{i1}E_{01}\right\rangle + \dots + \left|\hat{Y}_{iN}E_{0N}\right\rangle + \left|\hat{Y}_{i,c}E\right\rangle \\
\vdots \\
\left|J_{0N}\right\rangle = \left|\hat{Y}_{N1}E_{01}\right\rangle + \dots + \left|\hat{Y}_{NN}E_{0N}\right\rangle + \left|\hat{Y}_{N,c}E\right\rangle \\
\left|J\right\rangle = \left|\hat{Y}_{c,1}E_{01}\right\rangle + \dots + \left|\hat{Y}_{c,N}E_{0N}\right\rangle + \left|\hat{Y}_{c,c}E\right\rangle
\end{cases}$$
(2)

where:

 J_{oi} (i = 1 to N): current density of the source (i).

 $E_{\Delta^{oi}}^{oi}$ (*i* = 1 to *N*): Electric field of the source (i).

 $\hat{Y}_{ij}^{(m)}$ (i = 1 to N + 1, j = 1 to N + 1): Admittance operators with the $(N + 1)^{th}$ plan is the circuit plan.

 \vec{E} and \vec{J} are the electric field and the current density defined in the circuit plan.

The admittance operators \hat{Y}_{ij} define the relationship between the electric fields of TE and TM modes from the j^{th} plan and the current density generated by these modes on the i^{th} plan provided that all sources k # j are switched off and the circuit plan is metallized. These \hat{Y}_{ij} can be determined by applying the superposition theorem to the Equation (2) and decomposing these operators on a homogeneous basis (TE and TM) associated to each plan defining the different electromagnetic quantities.

to each plan defining the different electromagnetic quantities. If $(f_{mn})^{(j)}$ and $(f_{pq})^{(i)}$ are a decomposition orthonormal basis of the electric field and current density defined on the plans i and j, then for; $i=1,\cdots,N$ and $j=1,\cdots,N$, \hat{Y}_{ij} can be expressed as follows:

$$\hat{Y}_{ij} = \sum_{mn} \sum_{pq} \left| f_{pq}^{(i)} \right\rangle^{TE,TM} y_{mn,pq}^{TE,TM} \left\langle f_{mn}^{(j)} \right|^{TE,TM}. \tag{3}$$

For modeling the electric field E of the circuit plan, we choose test functions of electric field type φ_i . The test functions are assumed to be virtual sources defined in the circuit plan. On this basis, the electric field is written as follows:

$$E = \sum_{k=1}^{K} x_k \varphi_k. \tag{4}$$

With x_k ($k = 1, \dots, K$) are the projections of the field E in the test functions basis and K is the number of test functions necessary to reach convergence.

Test functions satisfy the boundary conditions of the circuit plan. They are zero on the metal and non-zero on the dielectric. And conversely, the current density J defined on the circuit plan is zero on the dielectric and non-zero on the metal. Therefore, the test function φ_k and the current density J satisfy the following relationship:

$$\langle \varphi_{\nu} | J \rangle = 0. \tag{5}$$

The application of the Galerkin method to the Equation (2) involves projecting the first N equations respectively on the unitary sources functions from $|e_{01}\rangle$ to $|e_{0N}\rangle$, which gives us a first sub system. Also, we project the $(N+1)^{\text{th}}$ equation on the various test functions which gives us a second sub system.

We suppose that: $E_{0i} = V_{0i}e_{0i}$ and $J_{0i} = I_{0i}j_{0i}$.

With $\langle e_{0i} | j_{0i} \rangle = 1$.

Considering the orthonormalization relationships verified by these unitary sources: (where δ_{ij} is the dirac function) and the relationship between current density and test functions, we obtain the following matrix relationship:

$$\begin{bmatrix} I_{01} \\ \vdots \\ I_{0N} \end{bmatrix} = \begin{bmatrix} y_{11} & \cdots & y_{1N} \\ \vdots & \ddots & \vdots \\ y_{N1} & \cdots & y_{NN} \end{bmatrix} \begin{bmatrix} V_{01} \\ \vdots \\ V_{0N} \end{bmatrix}. \tag{6}$$

With:

$$y_{ij} = Y_{ij} - C_i A^{-1} B_j. (7)$$

$$Y_{ij} = \left\langle e_{0i} \middle| \hat{Y}_{ij} e_{0j} \right\rangle. \tag{8}$$

$$A(i,k) = \langle \varphi_i | \hat{Y}_{N+1,N+1} \varphi_k \rangle. \tag{9}$$

$$B(j) = -\sum_{n=1}^{K} V_{0n} \left\langle \varphi_{j} \middle| \hat{Y}_{N+1,n} e_{0n} \right\rangle = -\sum_{n=1}^{K} V_{0n} B_{n}(j); j = 1, \dots, N$$
(10)

$$C(i) = -\sum_{n=1}^{K} V_{0n} \left\langle e_{0n} \left| \hat{Y}_{N+1,n} \varphi_{i} \right\rangle \right. = -\sum_{n=1}^{K} V_{0n} C_{n}(i); i = 1, \dots, N$$
(11)

The determination of the admittance matrix $(y_{ij})_{i,j=1,\cdots,N}$ involves calculating of the different admittance operators \hat{Y}_{ij} . In order to model the electric field E, it is enough to determine the coefficients of test functions x_k . The vector $X = (x_1, \dots, x_k, \dots, x_K)^t$ is written in the following form (Equation (12)):

$$X = -\sum_{i} V_{0i} A^{-1} B_i \tag{12}$$

In this section, we presented a general formulation of the problem by taking the case of N vertical sources. By applying the Galerkin method, we determined the admittance matrix binding current density and E field. This matrix is characterized by new admittance operators describing the transition from one plan to another. To validate our approach, we develop in this paper the case of a single vertical source that is the subject of the next section.

3. Single Vertical Source Case

The vertical source located at perpendicular plan to the circuit is the most used excitation source in real conditions. We focus now on the study of a single vertical source located at the perpendicular plan to the circuit. We start presenting the studied structures. Then, we detail the determination of the admittance operators.

3.1. Studied Structures

To validate our method, we consider two structures: a micro-strip short-circuited line **Figure 2(a)** and a micro-strip open circuit line **Figure 2(b)**. The choice of the first structure is argued by the fact that the estimated theoretical impedance of this structure is known which allows us to validate the obtained input impedance. The

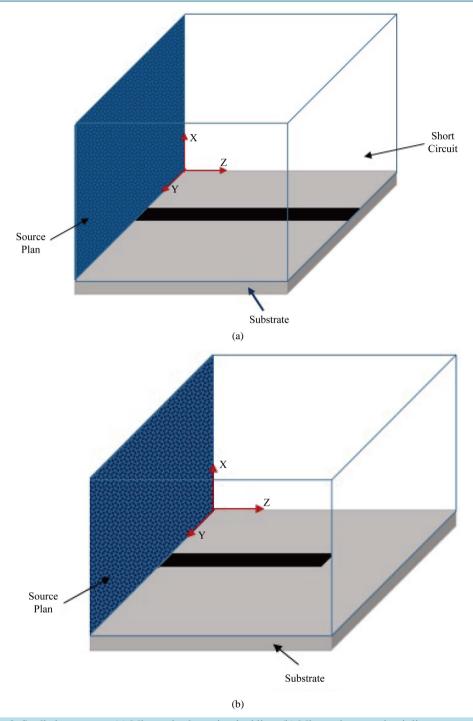


Figure 2. Studied structures: (a) Microstrip short-circuited line; (b) Microstrip open circuit line.

second structure allows us to verify the boundary conditions and to ensure the validity of the numerical approach.

Table 1 and Table 2 illustrate the dimensions of the two structures enclosed in a box.

With: $\varepsilon_r = 1$ and freq = 3.5 GHz,

 l_p : length of the microstrip line,

 l_s : length of the dielectric substrate,

 l_b : length of the box,

Table 1. Microstrip short-circuited line dimensions.

	Length	Width	Height
Line	$l_p = \lambda/2$	$w_p = 2 \text{ mm} \approx \lambda/40$	-
Dielectric substrate	$l_s = \lambda/2$	$a = 12.7 \text{ mm} \approx \lambda/7$	$h_0 = 1.27 \text{ mm} \approx \lambda/70$
Box	$l_b = \lambda/2$	$a = 12.7 \text{ mm} \approx \lambda/7$	$h = 11.43 \text{ mm} \approx 2\lambda/15$

Table 2. Microstrip open circuit line dimensions.

	Length	Width	Height
Line	$l_p = \lambda/2$	$w = 2 \text{ mm} \approx \lambda/40$	-
Dielectric substrate	$l_s = \lambda$	$a = 12.7 \text{ mm} \approx \lambda/7$	$h_0 = 1.27 \text{ mm} \approx \lambda/70$
Box	$l_b = \lambda$	$a = 12.7 \text{ mm} \approx \lambda/7$	$h = 11.43 \text{ mm} \approx 2\lambda/15$

 w_p : width of the microstrip,

a: width of the box/dielectric substrate,

 h_0 : thickness of the dielectric substrate,

h: thickness of the box.

By using the generalized equivalent circuit method, we can model each of the two structures of the Figure 2 with the following equivalent circuit (Figure 3).

The circuit is excited by a single source of electric field type. This source is defined by a unitary function $|e_1(x,y)\rangle$, such as: $\langle e_1,j_1\rangle=1$ and $|j_1\rangle$ is the current density associated to $|e_1(x,y)\rangle$ at the plan (xoy). The unitary source e_1 and the dual current j_1 are written as follows:

$$\left| e_{1} \right\rangle = \frac{\left| E_{d} \right\rangle}{\left\langle E_{d} \left| J_{d} \right\rangle}.\tag{13}$$

$$\left| j_{1} \right\rangle = \frac{\left| J_{d} \right\rangle}{\left\langle E_{d} \left| J_{d} \right\rangle}.\tag{14}$$

With: E_d is the electric field of the straight section and J_d is the dual current density

$$\boldsymbol{J}_{d} = \boldsymbol{H}_{d} \wedge \boldsymbol{z}. \tag{15}$$

To determine the electric field E_d , we calculate the fundamental E field of a microstrip line having infinite length [13] [19] [20].

3.2. The Input Admittance of a Discontinuity Port

Using the formulation of the source method developed in the Section 2, the current densities of the source and the circuit are associated to the corresponding electric fields by the admittance operators:

$$\begin{cases} \left| J_{1} \right\rangle = \left| \hat{Y}_{11} E_{1} \right\rangle + \left| \hat{Y}_{12} E_{2} \right\rangle \\ \left| J_{2} \right\rangle = \left| \hat{Y}_{21} E_{1} \right\rangle + \left| \hat{Y}_{22} E_{2} \right\rangle \end{cases}$$

$$(16)$$

Applying the same procedure in (2), the Galerkin method is used to solve Equation (16) while taking into account the boundary conditions of electromagnetic fields on the circuit plan. The first step in the Galerkin method is to define test functions $(\varphi_i)_{i=1,\dots,K}$ to model the electric field E_2 (Equation (17)).

$$E_2 = \sum_{i=1}^K x_i \varphi_i. \tag{17}$$

The test functions $(\varphi_i)_{i=1,\dots,K}$ should verify the boundary conditions imposed by the presence of metal in the

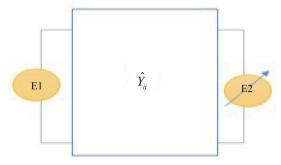


Figure 3. Equivalent circuit of the studied structure.

plan (xoz) (Equation (18)).

$$\varphi_{k}(x,z) = \begin{cases} E_{x}(x)\cos\left(\frac{(2k-1)\pi z}{2l}\right) \\ E_{z}(x)\sin\left(\frac{(2k-1)\pi z}{2l}\right) \end{cases}; x \in \left[0, \frac{a-w}{2}\right] \cup \left[\frac{a+w}{2}, a\right]$$
(18)

where:

 $E_{\scriptscriptstyle x}$ and $E_{\scriptscriptstyle z}$ are the electrical field components in circuit plan ($E_{\scriptscriptstyle c}$)

 $|e_2\rangle$ and $|j_2\rangle$ are defined in two complementary areas: the insulating areas and the metallic area.

The second step is to project the Equation (1) of the System (16) on the unitary function $|e_1\rangle$ and the Equation (2) on the various test functions to obtain the input admittance y_{in} :

$$y_{in} = \frac{I_1}{J_1} = Y_{11} - C_1 A^{-1} B_1.$$
 (19)

With:

$$Y_{11} = \left\langle e_1 \middle| \hat{Y}_{11} e_1 \right\rangle. \tag{20}$$

$$C_1(i) = \left\langle e_1 \middle| \hat{Y}_{12} \varphi_i \right\rangle. \tag{21}$$

$$A(i,j) = \langle \varphi_i | \hat{Y}_{22} \varphi_j \rangle. \tag{22}$$

$$B(i) = \langle \varphi_i | \hat{Y}_{21} e_1 \rangle. \tag{23}$$

To calculate the input admittance, we must determine the different admittance operators: $\hat{Y}_{11}, \hat{Y}_{12}, \hat{Y}_{22}$ and \hat{Y}_{21} which will be the object of the next section.

4. Admittance Operators

To calculate the different operators, we need to impose some conditions, namely: The \hat{Y}_{11} and \hat{Y}_{21} operators are determined by considering: $\left|E_{1}\right\rangle \neq 0$ and $\left|E_{2}\right\rangle = 0$ (metallization of circuit

Similarly, the \hat{Y}_{22} and \hat{Y}_{12} operators are calculated by considering $|E_2\rangle \neq 0$ and $|E_1\rangle = 0$ (metallization of source plan).

This procedure is ensured after establishing in each plan (xoy) and (xoz) a basis of TE and TM mode func-

tions satisfying the boundary conditions and allowing the decomposition of operators \hat{Y}_{ij} . We explain in the next two paragraphs the determination method of the operators \hat{Y}_{11} and \hat{Y}_{21} . The two others operators will be determined in the same manner.

To have an equation system containing only the two operators \hat{Y}_{11} and \hat{Y}_{21} , we must metalize the circuit

plan while taking $|E_2\rangle = 0$. So, the Equation (16) becomes:

$$\begin{cases} |J_1\rangle = |\hat{Y}_{11}E_1\rangle \\ |J_2\rangle = |\hat{Y}_{21}E_1\rangle \end{cases}$$
 (24)

Hence the circuit plan split the structure into two homogeneous areas separated by an electric wall. In the straight section of each guide, basis functions $f_{mn}(x, y)$ ($m, n = 1, \dots, N$) are defined by the TE and TM modes of the wall guide EEEE (E: Electric), relative to a propagation direction along the normal to this section plan.

The mode functions TE and TM in the plan (xoy) are given by the Equation (25) and Equation (26):

$$f_{mn}^{TE}(x,y) = \begin{cases} -\frac{n}{h_i} \xi_{mn} \cos\left(\frac{m\pi x}{a}\right) \sin\left(\frac{n\pi y}{h_i}\right) \\ -\frac{m}{a} \xi_{mn} \sin\left(\frac{m\pi x}{a}\right) \cos\left(\frac{n\pi y}{h_i}\right) \end{cases}$$
(25)

$$f_{mn}^{TM}(x,y) = \begin{cases} \frac{m}{a} \xi_{mn} \cos\left(\frac{m\pi x}{a}\right) \sin\left(\frac{n\pi y}{h_i}\right) \\ -\frac{n}{h_i} \xi_{mn} \sin\left(\frac{m\pi x}{a}\right) \cos\left(\frac{n\pi y}{h_i}\right) \end{cases}$$
(26)

With:

$$\xi_{mn} = \sqrt{\frac{2t_{mn}}{ah_i \left(\left(\frac{m}{a} \right)^2 + \left(\frac{n}{h_i} \right)^2 \right)}}.$$
(27)

$$\begin{cases} t_{mn} = 2; & \text{if } m, n \neq 0 \\ t_{mn} = 1; & \text{if } m \mid n = 0 \end{cases}$$

The propagation constant of the mode $f_{mn}(x, y)$ is γ_{mn} , such that:

$$\gamma_{mn}^2 = \left(\frac{m\pi}{a}\right)^2 + \left(\frac{n\pi}{h_i}\right)^2 - \omega^2 \varepsilon_i \mu_0. \tag{28}$$

$$\begin{cases} \varepsilon_i = \varepsilon_0 \varepsilon_r, & h_i = -h, & \text{for } : -h \le y \le 0 \\ \varepsilon_i = \varepsilon_0; & h_i = h_0, & \text{for } : 0 < y \le h_0 \end{cases}$$

4.1. Determination of the \hat{Y}_{11} Operator

The decomposition of the \hat{Y}_{11} operator on the TE and TM mode functions $f_{mn}^{TE,TM}(x,y)$ of the box is relative to the propagation constant (γ_{mn}) , defined as follows:

$$\hat{Y}_{11} = \sum_{mn} \left| f_{mn}^{TE,TM} \right\rangle y_{11,mn}^{TE,TM} \left\langle f_{mn}^{TE,TM} \right|. \tag{29}$$

With

 $y_{11,mn}^{TE}$ and $y_{11,mn}^{TM}$ are the admittance of TE and TM modes brought from the short circuit (z = 1) to the source plan (z = 0).

$$y_{11,mn}^{TE} = \frac{\gamma_{mn}}{j\omega\mu} \coth(\gamma_{mn}l). \tag{30}$$

$$y_{11,nm}^{TM} = \frac{j\omega\varepsilon_i}{\gamma_{mn}} \coth(\gamma_{mn}l). \tag{31}$$

4.2. Determination of the \hat{Y}_{21} Operator

The operator \hat{Y}_{21} describes the transition of the plan (xoy) to the plan (xoz). So that, from the field generated in the plan (xoy), we find the current density in the other plan. Therefore, we must determine the field created in the whole structure.

In fact, knowing the electric field in the source plan (xoy) which is decomposed on the mode functions $f_{mn}^{TE,TM}\left(x,y\right)$ we deduce the field created in the whole structure. For the components of $f_{mn}^{TE,TM}\left(x,y,z\right)$ on the

axes (ox) and (oy), we use the following relationship:
$$f_{mn}^{TE,TM}\left(x,y,z\right) = f_{mn}^{TE,TM}\left(x,y\right) \frac{\sinh\left(\gamma_{mn}\left(l-z\right)\right)}{\sinh\left(\gamma_{mn}l\right)}$$
.

The third component of $f_{mn}^{TE,TM}(x,y,z)$ on the axis (oz) is deduced it from the Gauss Maxwell equation (div(E)=0).

Equation (32) presents the relationship binding the operator \hat{Y}_{21} to the current density J_2 (of the plan P_2 (xoz)) and the electric field E_1 (of the plan P_1 (xoy)).

$$\left|J_{2}\right\rangle = \left|\hat{Y}_{21}E_{1}\right\rangle. \tag{32}$$

To describe the operator \hat{Y}_{21} , we define a second basis of the TE and TM modes functions g_{pq} (EEEE wall box) which satisfy the boundary conditions for the current density in the plan (xoz) and describes the current J_2 on this basis (Equation (33)).

$$J_2 = \sum_{pq} J_{pq}(x, z) = \sum_{pq} I_{pq} g_{pq}.$$
 (33)

Similarly, we can describe the field E_1 on the basis of mode functions f_{mn} as follows (Equation (34)):

$$E_{1} = \sum_{mn} E_{mn}(x, y, z) = \sum_{mn} e_{mn} f_{mn}(x, y, z).$$
(34)

Using the Equation (34), we can express the current J_2 using the basis functions f_{mn} (Equation (35)).

$$\left|J_{2}\right\rangle = \sum_{mn} \left|\hat{Y}_{21} E_{mn}\right\rangle = \sum_{mn} e_{mn} \left|\hat{Y}_{21} f_{mn}\right\rangle. \tag{35}$$

By identification (Equation (33) and Equation (35)), the current J_2 could be expressed as follows:

$$J_2 = \sum_{pq} \sum_{mn} e_{mn} \left\langle g_{pq} \left| \hat{Y}_{21} f_{mn} \right\rangle \right| g_{pq} \right\rangle. \tag{36}$$

Using Equation (33) and the fact that f_{mn} is an orthonormal basis, Equation (36) becomes:

$$J_{2} = \sum_{pq} \sum_{pq} \left| g_{pq} \right\rangle \left\langle g_{pq} \left| \hat{Y}_{21} f_{mn} \right\rangle \left\langle f_{mn} \left| E_{1} \right\rangle \right. \tag{37}$$

From the Expression (37), we deduce the expression of the \hat{Y}_{21} operator

$$\hat{Y}_{21} = \sum_{p_{0}} \sum_{mm} \left| g_{pq} \right\rangle y_{mn} \left\langle f_{mn} \right|. \tag{38}$$

With:

$$y_{mn} = \left\langle g_{pq} \middle| \hat{Y}_{21} f_{mn} \right\rangle. \tag{39}$$

We define $\hat{R}_{21} = |\hat{Y}_{21}f_{mn}\rangle$ as a new operator permits the passage of the plan P_1 to the plan P_2 . To deduce this new operator, we establish an expression for the current density in the plan (xoz) while using Maxwell equations. Applying the Maxwell-Faraday equation, the current density has the following expression (Equation (40)):

$$J = \frac{-1}{i\omega\mu} (\nabla \wedge E) \wedge y. \tag{40}$$

By substituting the expression of E_1 (Equation (34)) in the expression of J (Equation (40)), we obtain:

$$J = \frac{-1}{j\omega\mu} \sum_{mn} e_{mn} \left(\frac{\partial f_x(x,y)}{\partial y} - \frac{\partial f_y(x,y)}{\partial x} \right) \frac{\sinh(\gamma_{mn}(l-z))}{\sinh(\gamma_{mn}l)} \frac{\partial f_z(x,y,z)}{\partial y} - \frac{\partial f_y(x,y,z)}{\partial z} \right)_{y=0}$$
(41)

By identification between Equations (36) and (41), we can deduce the expression of the new operator \hat{R}_{21} based on a rotational transformation (Equation (42)).

After expressing the different operators and transformations, the next section will be dedicated to numerical results for two chosen structures and make some comparisons to validate our new approach.

5. Numerical Results

The new numerical approach is based on the definition of several admittance operators used to describe the passage from one plan to another. The implementation of these operators require several large-sized matrices manipulation and cpu-consuming integral calculations. In our case, using development environments dedicated to numerical calculations such as MATLAB is not suitable, lacks of fast hybrid symbolic/numeric calculation and has no built-in cache support neither save-points concept (we cannot resume calculation when needed).

There are several alternatives, namely programming languages: C, C++ and Java. In literature, several researchers recommended JAVA for scientific treatment [23]-[25] due to its robustness, automatic memory management and portability.

In our research laboratory SYS'COM, DrTaha Ben Salah has developed during his research work a TMWLib library (for Tiny MicroWave Library) [26]. This library is based on Java/Scala programming languages, is fully modular, feature rich and scalable. Also, it enables fast hybrid symbolic/numerical calculation and cache/save-point concept.

We applied our modelling approach to both structures: microstrip short-circuited line and microstrip open circuit line. We used the microstrip short-circuited line as a reference structure to compare obtained input impedance with theoretical input impedance of this structure. We also deduce for these structures some electromagnetic characteristics (current density J and electric field E) to verify the boundary conditions.

5.1. Input Admittance of Microstrip Short-Circuited Line

The chosen studied structure to validate the obtained input impedance is a microstrip short-circuited line. This structure must respect two approximations. First, the structure is considered as a transmission line submitted to the line's fundamental mode (characterized by its propagation constant β_g). Then, the line length L should be large enough to assume that higher order modes reflected at the short circuit are attenuated before reaching excitation source. The expected value of the theoretical input admittance is given by the Equation (43).

$$y_{in}^{th} = -j \coth(\beta l) \tag{43}$$

With β is the propagation constant of the fundamental mode.

Figure 4 illustrates the simulation result of the input impedance y_{in} given by Equation (20) and that of the theoretical input impedance of a line short circuited. The y_{in} curve is evaluated at convergence with 11 test

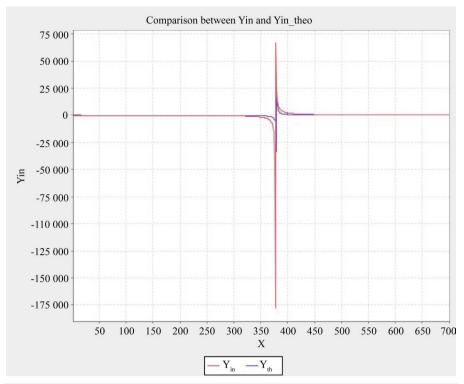


Figure 4. Comparison between the calculated and theoretical input admittance for microstrip short-circuited line.

functions (trigonometric type), 94,000 TE and TM mode functions f_{mn} (on source plan) and 68,900 TE and TM modes functions g_{pq} (on circuit plan).

We observe that the two curves of the input impedance are very close with a relative error lower than 1%. This confirms the validity of our numerical approach and the perfect adaptation between source and circuit. In fact, among the parameters affecting the consistency of results precision of the fundamental mode taken as excitation source has the higher effect.

With X is the length 1 of the microstrip line.

5.2. Electromagnetic Characteristics of Microstrip Short-Circuited Line

In this section, we present some electromagnetic characteristics (current density and electric field) for a microstrip short-circuited line. We also compare the obtained results to the results found with two commercial software HFSS and CST.

Figure 5 illustrates shapes of the current density (HFSS, CST and MoM-GEC'3D). We observe that the three curves have the same variation of the current which satisfies the boundary conditions. This result is very consistent with excepted values for a short-circuited line for the specified dimensions.

Figure 6 and Figure 7 illustrate the shape of the electric field E_y along the propagation direction (oy). We note that the electric field satisfies the boundary conditions. It is maximum at the source and presents a fast attenuation at source/line discontinuity. The result with the new approach MOM-GEC'3D contains small attenuation that tend to cancel due to the Gibbs effect. HFSS gives a less step attenuation at source/line discontinuity; whereas CST's result has the best consistency

Figure 8 allows to verify the boundary conditions for the electric field E_x . The two figures obtained with CST and MoM-GEC'3D confirms results alignment.

5.3. Electromagnetic Characteristics of Microstrip Open Circuit Line

In this section, we present results of current density and electric field for a microstrip open circuit line to validate our numerical approach.

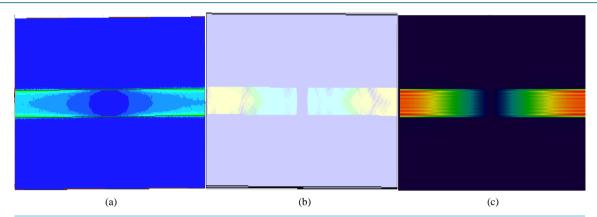


Figure 5. Current density comparison between: (a) HFSS; (b) CST and (c) MoM-GEC'3D.

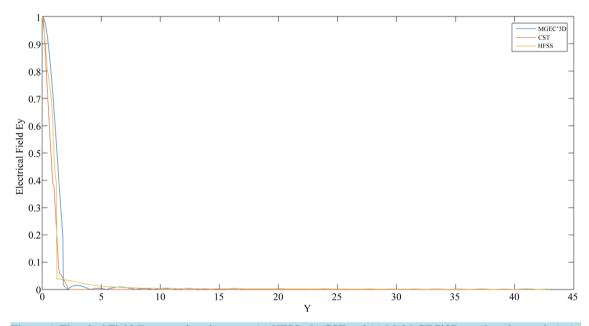


Figure 6. Electrical Field E_y comparison between: (a) HFSS; (b) CST and (c) MoM-GEC'3D: section along axis (ox).

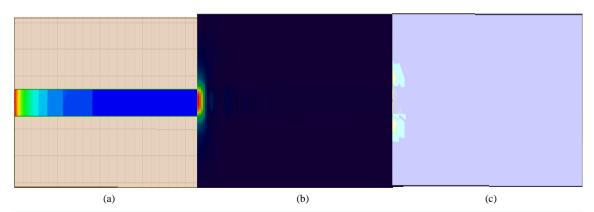


Figure 7. Electrical Field E_y comparison between: (a) HFSS; (b) MoM-GEC'3D and (c) CST: 2D view.

Figure 9 and Figure 10 present the current density. We note that the three results obtained with MoM-GEC'3D, HFSS and CST are very consistent. At convergence MoM-GEC'3D and CST present better results when considering EM symmetry of the structure (along ox axis). Besides, line width is small when compared to

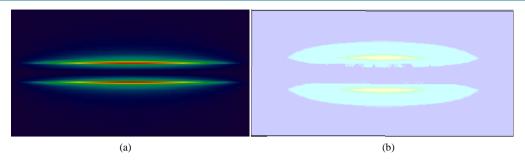


Figure 8. Electrical Field E_x comparison betweenMoM-GEC'3D and CST.

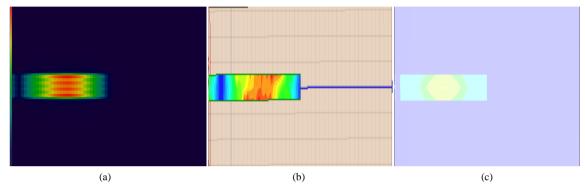


Figure 9. Current density J_v comparison between: (a) MoM-GEC'3D; (b) HFSS and (c) CST.

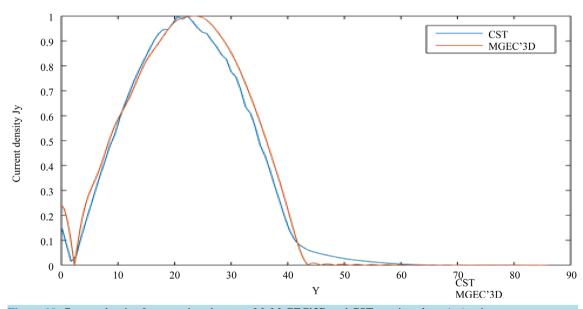


Figure 10. Current density J_y comparison between MoM-GEC'3D and CST: section along (ox) axis.

wavelength, variations along (x) should not be relevant, which is the case of (a). Still CST have some important variation (at the center of the line). Moreover, a better attenuation (Figure 10) is remarkable with small Gibbs effect lead us to conclude that new approach gives more rigorous results.

Figure 11 and Figure 12 illustrate that the electric field for the 3 simulation results verify the boundary conditions. We note also that the curve of MOM-GEC'3D contains small attenuation due to Gibbs effect. We may add an artificial additional term to compensate this effect and reduce numerical errors. Figure 12 gives even better confirmation of result validation as one can appreciate good attenuation of numerical results of E_y away from source and magnetic wall (line edge). Again, MoM-GEC'3D gives better results but still very similar to

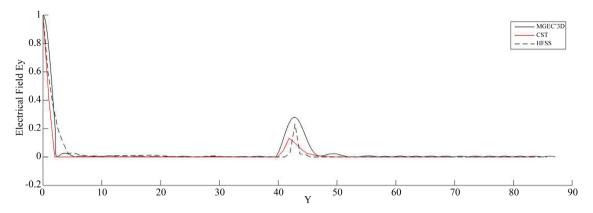


Figure 11. Electrical Field E_y section along (oy): comparison between MOM-GEC'3D, CST and HFSS.

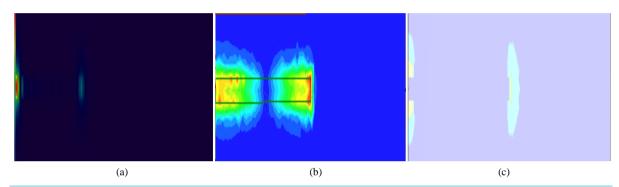


Figure 12. Electrical Field E_y comparison between (a) MOM-GEC'3D, (b) HFSS and (c) CST.

CST while HFSS gives a little more fuzzy results. All of three results still consistent with boundary conditions though.

Similarly, the electric field component E_x verifies the boundary conditions for the result obtained with MOM-GEC'3D and CST while CST, for this case, presents a better boundary conditions. This may be explained with the forced usage of (y) based test functions (in order to validate more generic approach) whereas line width is too small relatively to wavelength, so that Gibbs effect remains a little substantial (Figure 13).

The different simulations made for short circuit and open circuit demonstrates the accuracy of our new approach. This was approved by the verification of the boundary conditions and comparison with two commercial simulation software HFSS and CST.

6. Conclusions

In this paper, we present a new formulation of the source method to characterize discontinuities in planar circuits. A new definition of the excitation source is introduced to overcome the discontinuity problem at the source/circuit transition. We expose a general formulation of the source method, by determining the Input admittance matrix of N-port discontinuity in a planar circuit. To validate our approach, we considered the case of a single vertical source. We detailed the determination of the various operators and rotational transformations required to calculate the input impedance. In the last part of our work, we presented and interpreted some results in the case of a microstrip short-circuited line and microstrip open circuit line.

The numerical results obtained using this approach were compared to results obtained by both commercial software HFSS and CST. Our results show a concordance and consistency with those obtained using HFSS and CST, with even better results in most cases.

We also demonstrated that considering the fundamental mode of the access line to circuit as the excitation source gives us a perfect adaptation between the source and the circuit.

This new approach can be applied to any type and number of excitation sources (coaxial cable at perpendicular plan [27] or coaxial line located in the ground plan, etc). The originality of this work lies mainly in the new

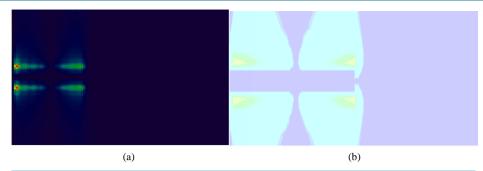


Figure 13. Electrical Field E_x comparison between MGEC and CST: 2D view.

definition of the source in the integral analysis and the determination of admittance operators to link the electromagnetic quantities of sources and magnitudes of the circuit, which are defined in vertical plans.

References

- [1] Harrington (1961) Time Harmonic Electromagnetic Fields. Mac Graw Hill.
- [2] Kunz, K. and Luebber, R. (1993) The Finite Difference Time Domain Method for Electromagnetics. CRC, Boca Raton.
- [3] Cherry, P.C. and Iscander, M.F. (1995) FDTD Analysis of High Frequency Electronic Interconnection Effects. *IEEE Transactions on Microwave Theory Tech*, **43**, 2445-2451. http://dx.doi.org/10.1109/22.466178
- [4] Ferrari, R.L. and Naidu, R.L. (1990) Finite-Element Modeling of High-Frequency Electromagnetic Problems with Material Discontinuities. IEEE Proc, 137, 313-320.
- [5] Lee, J.F. and Sacks, Z. (1994) WETD—A Finite Elements Time Domain Approach for Solving Maxwell's Equations. *IEEE Microwave Guided Wave Letters*, **4**, 11-13. http://dx.doi.org/10.1109/75.267679
- [6] Hamdi, N., Aguili, T., Bouallegue, A. and Baudrand, H. (1998) A New Technique for the Analysis of Discontinuities in Microwave Planar Circuits. *Progress in Electromagnetic Research*, *PIER*, 21, 137-151. http://dx.doi.org/10.2528/PIER98051101
- [7] Aguili, T. (2000) Modélisation des composants S. H. F planairespar la méthode des circuits équivalentsgénéralisés. Ph.D. Thesis, National Engineering School of Tunis ENIT.
- [8] Harrington, R.F. (1983) Field Computation by Moment Methods. MacMillan, New York.
- [9] Ooms, S. and De Zutter, D. (1998) A New Iterative Diakoptics-Based Multilevel Moments Method for Planar Circuits. *IEEE Transactions on Microwave Theory and Techniques*, **46**, 288-291. http://dx.doi.org/10.1109/22.661716
- [10] Aguili, T., Graya, K., Boualegue, A. and Baudrand, H. (1996) Application of a Source Method for Modeling Step Discontinuities in Microstrip Circuit. *IEE Proceedings—Microwaves*, Antennas and Propagation, 143, 169-173. http://dx.doi.org/10.1049/ip-map:19960224
- [11] Hamdi, N., Aguli, T. and Boualegue, A. (1998) Considération des sources dans la caractérisation des discontinuities dans les guides d'ondescoplanaires. 6ème colloque Maghrébinsur les Modèles Numériques de l'ilngénieur, Tunis, 24-26 November 1998.
- [12] Hamdi, N., Aguili, T. and Boualegue, A. (1998) Caractérisation des discontinuitésdans les circuits planaires micro-ondes avec une nouvelle technique intégrale. *Mediterranean Conference on Electronics and Automatic Control*, Marrakech Maroc, 17-19 September 1998.
- [13] Hamdi, N. (1999) Analyse des Discontinuitésdans les circuits planaires par une Nouvelle Formulation de la méthode des Sources. PhD Thesis, National Engineering School of Tunis, ENIT, Tunis.
- [14] El Gouzi, M.E.A. and Boussouis, M. (2010) Hybrid Method for Analyse Discontinuities in Shielded Microstrip. *International Journal of Engineering Science and Technology*, **2**, 3326-3334.
- [15] Pujol, S., Baudrand, H. and Hanna, F. (1993) A Complete Description of a Source Type Method for Modelling Planar Structures. *Annals of Telecommunications*, **48**, 459-470.
- [16] Graya, K., Aguili, T., Boualegue, A. and Boudrand, H. (1999) Characterization of Planar Passive Circuits Using Source Method in Conjunction with Three Different Sets of Trial Functions. *IEE Proceeding-Microwaves, Antennas and Propagation*, **146**, 209-213. http://dx.doi.org/10.1049/ip-map:19990628
- [17] Graya, K., Aguili, T. and Boualegue, A. (1998) Characterization of Planar Passive Circuits Using Source Method. *International Conference on Microelectronics, ICN*'98, Monastir, 14-16 December 1998, 131-134.

http://dx.doi.org/10.1109/icm.1998.825586

- [18] Graya, K., Aguili, T. and Boualegue, A. (1999) Etude de discontinuitésplanaires en microruban par uneméthode de source avec différents types d'excitations, Journéessur les télécommunications, Tunisia, 29-31 January 1999.
- [19] Golio, M. and Golio, J. (2007) RF and Microwave Passive and Active Technologies. RF and Microwave Handbook, Second Edition, CRC Press, Boca Raton. http://dx.doi.org/10.1201/9781420006728
- [20] Hitendra, K., Malik, A. and Singh, K. (2009) Engineering Physics.
- [21] El Gouzi (2015) Modélisation des discontinuitesuni-axialesdans les circuits planairespar la méthode des moments. PhD Thesis, Université Abdelmalek Essaadi, Tétouan.
- [22] Ourabia, M. (2014) Modelling and Characterization of Planar Components. Advanced Materials Research, 1025-1026, 1055-1061. http://dx.doi.org/10.4028/www.scientific.net/amr.1025-1026.1055
- [23] Lea, D. (1997) Concurrent Programming in Java. Addison Wesley Pub., Boston.
- [24] Weems, C.C., Weaver, G.E. and Dropsho, S.G. (1994) Linguistic Support for Heterogeneous Parallel Processing: A Survey and an Approach. *Proceedings of the Heterogeneous Computing Workshop*, Cancun, 26 April 1994, 81-88.
- [25] Weems, C. (1998) Heterogeneous Programming with Java: Gourmet Blend or Just a Hill of Beans? *Proceedings of the 7th Heterogeneous Computing Workshop*, Orlando, 30 March 1998, 173-182.
- [26] Salah, T.B. (2009) Etude des structures invariantes par échelles par uneméthodeintégrale multi modalecombinée à la méthode de normalization. PhD Thesis, National Engineering School of Tunis, ENIT, Tunis.
- [27] Oueslati, B., Salah, T.B., Chiraz, L. and Aguili, T. (2016) Rigorous Study of a Microstrip Line Excited with Perpendicular Coaxial Source Using MGEC MoM Based Method. 3rd International Conference on Automation, Control Engineering and Computer Science, ACECS-2016, Hammamet, 20-22 March 2016.