

An Active-Only Temperature-Insensitive Current-Mode Biquad Filter Based on Differentiator Structures Employing CCCCTAs

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ABSTRACT

This article presents an active-only current-mode universal biquad filter performing three standard functions: low-pass, high-pass and band-pass function, which can be readily modified to achieve the rest functions (band-stop and all-pass). The circuit principle is based on active-only circuit designed by using differentiators which are constructed from current controlled current conveyor transconductance amplifier (CCCCTA) cooperating with an internally frequency compensated operational amplifier (OA). The features of the circuit are that: the pole frequency and quality factor can be independently tuned via the input bias currents and it is ideally temperature-insensitive, its circuit description is very simple, consisting of 3 CCCCTAs and 2 operational amplifiers, and the proposed circuit is very appropriate for further developing into integrated circuit architecture. The PSpice simulation results are shown. The given results agree well with the theoretical anticipation.

Keywords: Active-Only; CCCCTA; Current-Mode; Biquad Filter

1. Introduction

An analog filter is an important building block, widely used for continuous-time signal processing. It can be found in many fields: including, communications, measurement, instrumentation, and control systems [1,2]. One of the most popular analog filters is a universal biquad filter, since it can simultaneously provide several functions in the same circuit topology. Recently, a universal filter working in current-mode has been more popular than the voltage-mode type. Since last two decades, there has been much effort to reduce the supply voltage of analog systems. This is due to the demand for portable and battery-powered equipment. Since a low-voltage operating circuit becomes necessary, the current-mode technique is ideally suited for this purpose. Actually, a circuit using the current-mode technique has many other advantages, such as, larger dynamic range, higher bandwidth, greater linearity, simpler circuitry and lower power

consumption [3,4].

The synthesis and design of analog signal processing circuits using only active elements without passive elements are important in fully integrated circuit (IC) technology. This technique makes circuit become smaller chip area, lower power consumption, wider frequency range of operation and programmability [5-8], where the applications can be easily seen in many literatures, for example filter [7], oscillator [9], inductance simulator [10] and etc.

From the past, creation of differentiator circuit must use an inductor worked together with an active element which affected on circuit as large sized. So, it was not popular to create circuit with differentiator. But nowadays, we can design differentiator-based circuit without any inductor, and then causing a reduction in circuit sized smaller than creation circuit in the past. Biquadratic transfer function is widely used in order to synthesize the filters. Many kinds of filters can be realized based on only integrators as building blocks [11-14]. However,

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integrator circuit performs as a low-pass filter. At high frequency, stage-gain of integrator is decayed due to components bandwidth and integrator characteristic. In order to realize filter, magnitude response of filter in high-frequency would be unstable according to the mentioned characteristics. Differentiator circuit can perform as a high-pass filter that compensated with component bandwidth for stabilization of magnitude response in high-frequency [15].

A reported 5-terminals active element, namely current convey transconductance amplifier (CCTA) [16] has been proposed in 2005, it seems to be a versatile component in the realization of a class of analog signal processing circuits, especially analog frequency filters. It is a really current-mode element whose input and output signals are currents. In addition, the output current gain can be adjusted via input bias current. However, the parasitic resistance at current input port of the CCTA cannot be controlled. Recently, Siripruchyanun and Jaikla have proposed the modified-version CCTA, whose parasitic resistance at current input port can be controlled by an input bias current. It is newly named current controlled current conveyor transconductance amplifier (CCCCTA) [17]. It seems to be a useful building block, since many circuits and systems can be implemented by employing only single CCCCTA. Presently, the CCCCTA has been extensively used, such as filters [18,19], oscillators [20, 21], inductance simulators [22], and etc.

From our recent survey, it is found that several implementations of current-mode universal biquad filters using active-only principle have been reported. Unfortunately, these reported circuits suffer from one or more of following weaknesses:

- Excessive use of the active elements [23,24].
- Lack of electronic adjustability [23,24].
- The pole frequency and quality factor cannot be independently tuned [18,25].

The aim of this paper is to propose a new current-mode universal biquad filter. The features of proposed circuit are that: it employs 3 CCCCTAs and 2 internally frequency compensated operational amplifiers; the proposed universal biquad filter can provide 3 standard functions including low-pass, high-pass and band-pass functions in the same time without changing circuit topology, where the rest functions (band-stop and all-pass) can be readily obtained by small modification; the circuit description is very simple, which results in a small-size of the monolithic chip. In addition, it is electronically tunable and convenient to use. The quality factor and pole frequency can be electronically and independently adjusted. The PSpice simulation results are also shown, which are in correspondence with the theoretical analysis.

2. Theory and Principle

2.1. Implementation Topology of the Proposed Filter

The filter is designed by cascading current adder and the current-mode lossless differentiators fed-back by current amplifier whose gain of k as systematically shown in **Figure 1**. From block diagram in **Figure 1**, its transfer function can be found to be

$$\frac{I_{LP}}{I_{in}} = \frac{1}{s^2 + \frac{sk}{b} + \frac{1}{ab}}, \quad (1)$$

$$\frac{I_{BP}}{I_{in}} = \frac{s}{s^2 + \frac{sk}{b} + \frac{1}{ab}}, \quad (2)$$

and

$$\frac{I_{HP}}{I_{in}} = \frac{s^2}{s^2 + \frac{sk}{b} + \frac{1}{ab}}. \quad (3)$$

From Equations (1)-(3), the pole frequency (ω_0) and quality factor (Q_0) can be respectively expressed as

$$\omega_0 = \sqrt{\frac{1}{ab}}, \quad (4)$$

and

$$Q = \frac{1}{k} \sqrt{\frac{b}{a}}. \quad (5)$$

It is found that the pole frequency can be adjusted by either a or b , by keeping their ratio to be constant where the quality factor can be tuned through k without effecting the pole frequency.

2.2. Basic Concept of CCCCTA

The principle of the CCCCTA was firstly published in 2008 by Siripruchyanun and Jaikla [17]. The schematic symbol and the ideal behavioral model of the CCCCTA are shown in **Figures 2(a)** and **(b)**, respectively. The characteristics of the ideal CCCCTA are represented by

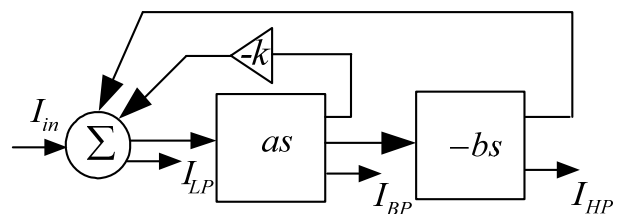


Figure 1. Block diagram for proposed filter implementation.

the following equation:

$$\begin{bmatrix} I_y \\ V_x \\ I_{z,z_c} \\ I_o \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 & 0 \\ R_x & 1 & 0 & 0 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & \pm g_m & 0 \end{bmatrix} \begin{bmatrix} I_x \\ V_y \\ V_z \\ V_o \end{bmatrix} \quad (6)$$

If the CCCCTA is realized using a BJT technology, R_x and g_m can be respectively written as

$$R_x = \frac{V_T}{2I_{B1}}, \quad (7)$$

and

$$g_m = \frac{I_{B2}}{2V_T}, \quad (8)$$

where g_m is transconductance of the CCCCTA. V_T is the thermal voltage equal to 26 mV at a room temperature. I_{B1} and I_{B2} are the bias currents used to control the parasitic resistance and transconductance of the CCCCTA. In general, CCCCTA can contain an arbitrary number of terminals, providing currents I_o of both directions, respectively. A possible BJT implementation of the CCCCTA is shown in **Figure 3**.

2.3. Internally Frequency Compensated Operational Amplifier

The open-loop gain of a practical internally frequency

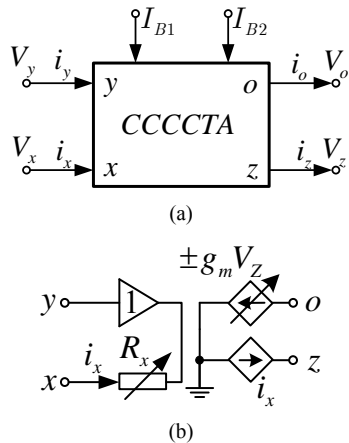


Figure 2. CCCCTA (a) Symbol (b) Equivalent circuit.

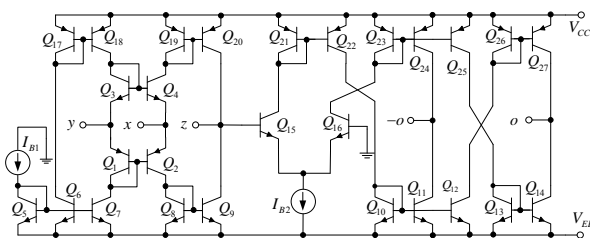


Figure 3. Schematic of the BJT CCCCTA.

compensated operational amplifier (OA) is represented by the following transfer function

$$A(s) = \frac{A_o \omega_{p1}}{s + \omega_{p1}} = \frac{B}{s + \omega_{p1}}, \quad (9)$$

where A_o is open-loop DC gain, ω_{p1} is the first pole frequency and $B (= A_o \omega_{p1})$ is the gain-bandwidth product of the operational amplifier. For the frequencies $\omega \gg \omega_{p1}$, Equation (9) is approximately given by [5,7]

$$A(s) \cong \frac{B}{s}. \quad (10)$$

3. Proposed Current-Mode Biquad Filter

As mentioned in last section, the proposed filter is based on current amplifier and the current-mode lossless differentiators. In this section, these circuits will be described. The current amplifier based on the CCCCTA is shown in **Figure 4**. The output current of this circuit can be written to be

$$I_{OutA} = kI_{inA}, \quad (11)$$

where $k = g_m R_x / 2$. **Figure 5** shows the lossless differentiator using the CCCCTA. Considering the circuit in **Figure 5** and using the CCCCTA properties, we will receive

$$\frac{I_{OutB}}{I_{inB}} = sa, \quad (12)$$

where $a = \frac{R_x g_m}{B}$.

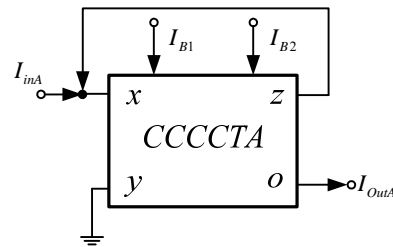


Figure 4. Current amplifier based on CCCCTA.

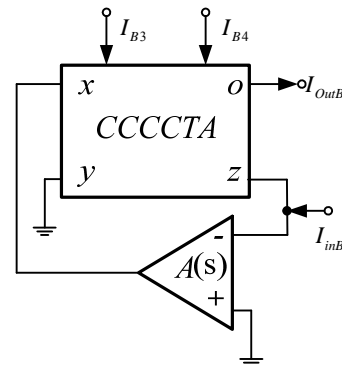


Figure 5. Lossless differentiator using CCCCTA and OA.

The completed current-mode biquad filter is shown in **Figure 6**, based on filter topology in **Figure 1**. From Equation (12), if $B_1 = B_2 = B$ for easy consideration, the output current transfer functions of the circuit at each output terminal in **Figure 6** can be obtained as

$$\frac{I_{LP}}{I_{in}} = \frac{\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}}{s^2 + s\frac{R_{x3}g_{m3}}{2}\left(\frac{B}{R_{x2}g_{m2}}\right) + \left(\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}\right)}, \quad (13)$$

$$\frac{I_{BP}}{I_{in}} = \frac{\frac{sB}{R_{x2}g_{m2}}}{s^2 + s\frac{R_{x3}g_{m3}}{2}\left(\frac{B}{R_{x2}g_{m2}}\right) + \left(\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}\right)}, \quad (14)$$

$$\frac{I_{HP}}{I_{in}} = \frac{s^2}{s^2 + s\frac{R_{x3}g_{m3}}{2}\left(\frac{B}{R_{x2}g_{m2}}\right) + \left(\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}\right)}, \quad (15)$$

Moreover, the band-stop and the all-pass functions can be further obtained, combining the currents $I_{BS} = I_{HP} + I_{LP}$ and $I_{AP} = I_{BS} - I_{BP}$, respectively, where

$$\frac{I_{BS}}{I_{in}} = \frac{s^2 + \frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}}{s^2 + s\frac{R_{x3}g_{m3}}{2}\left(\frac{B}{R_{x2}g_{m2}}\right) + \left(\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}\right)}, \quad (16)$$

$$\frac{I_{AP}}{I_{in}} = \frac{s^2 - \frac{sB}{R_{x2}g_{m2}} + \frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}}{s^2 + s\frac{R_{x3}g_{m3}}{2}\left(\frac{B}{R_{x2}g_{m2}}\right) + \left(\frac{B^2}{R_{x1}R_{x2}g_{m1}g_{m2}}\right)}. \quad (17)$$

The pole frequency (ω_0) and quality factor (Q_0) can be respectively expressed to be

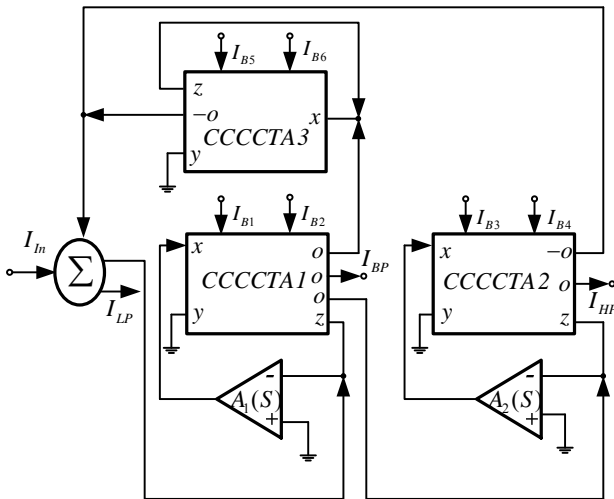


Figure 6. Completely proposed current-mode biquad filter.

$$\omega_0 = B\sqrt{\frac{1}{R_{x1}R_{x2}g_{m1}g_{m2}}}, \quad (18)$$

and

$$Q = \frac{1}{k}\sqrt{\frac{R_{x2}g_{m2}}{R_{x1}g_{m1}}}. \quad (19)$$

Substituting $R_{x1} = V_T/2I_{B1}$, $R_{x2} = V_T/2I_{B3}$, $R_{x2} = V_T/2I_{B5}$, $g_{m1} = I_{B2}/2V_T$, $g_{m2} = I_{B4}/2V_T$ and $g_{m3} = I_{B6}/2V_T$, the pole frequency and quality factor can be rewritten to be

$$\omega_0 = 4B\sqrt{\frac{I_{B1}I_{B3}}{I_{B2}I_{B4}}}, \quad (20)$$

and

$$Q = \frac{8I_{B5}}{I_{B6}}\sqrt{\frac{I_{B1}I_{B4}}{I_{B2}I_{B3}}}. \quad (21)$$

It is obviously found that, from Equations (20) and (21), the pole frequency can be adjusted independently from the quality factor by varying either I_{B1} and I_{B3} or I_{B2} and I_{B4} (keeping their ratio to be constant), where the quality factor can be adjusted by I_{B5} or I_{B6} without affecting the pole frequency. In addition, if the gain-bandwidth product (B) is independent of temperature variation, it is seen that the pole frequency and quality factor are ideally temperature insensitive.

4. Sensitivity Analysis

The sensitivities of the proposed circuit can be found as

$$S_{I_{B1},I_{B3}}^{\omega_0} = \frac{1}{2}, S_{I_{B2},I_{B4}}^{\omega_0} = -\frac{1}{2}, S_B^{\omega_0} = 1, \quad (22)$$

and

$$S_{I_{B5}}^{Q_0} = 1, S_{I_{B6}}^{Q_0} = -1, S_{I_{B1},I_{B4}}^{Q_0} = \frac{1}{2}, S_{I_{B2},I_{B3}}^{Q_0} = -\frac{1}{2} \quad (23)$$

Therefore, all active and passive sensitivities are equal or less than unity in magnitude.

5. Simulation Results

The working performances of the proposed biquad filter were verified in PSpice simulation using the BJT implementation of the CCCCTA as shown in **Figure 3**. The PNP and NPN transistors employed in the proposed circuit were simulated by using the parameters of the PR200N and NR200N bipolar transistors of ALA400 transistor array from AT&T [26]. The OA was simulated by using the parameter of TL082 with the gain-bandwidth product of $B = 4$ MHz is used. The circuit was biased with ± 5 V power supplies voltage, $I_{B1} = I_{B3} = 30 \mu A$, $I_{B2} = I_{B4} = 400 \mu A$, $I_{B5} = 50 \mu A$ and $I_{B6} = 100 \mu A$, using ideal current summing circuit. The

results shown in **Figure 7** are the gain responses of the proposed biquad filter. It is clearly seen that it can provide low-pass, high-pass, band-pass functions without modifying any circuit topology. **Figure 8** displays gain response for band-stop function as depicted in Equation (16). **Figure 9** displays gain and phase responses for all-pass function analyzed in Equation (17).

Figure 10 displays gain responses of band-pass function, where I_{B1} and I_{B3} are equally set to keep its ratio to be constant and changed for several values. **Figure 11** shows gain responses of band-pass function, where I_{B2} and I_{B4} are equally set to keep the ratio to be constant and changed for several values. **Figures 12** and **13** are gain responses of band-pass function for different I_{B5} and I_{B6} values, respectively. It is shown that the bandwidth of the responses can be electronically adjusted by the input bias current I_{B5} and I_{B6} without affecting the pole frequency. **Figure 14** shows gain re-

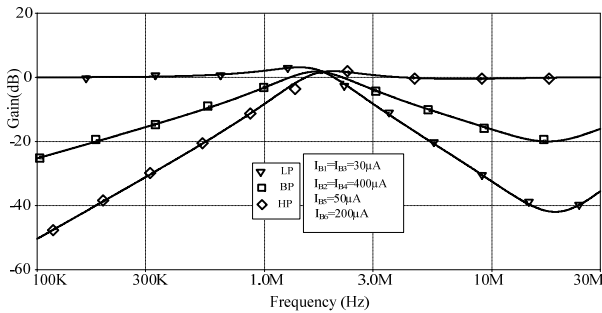


Figure 7. Gain responses of the proposed circuit.

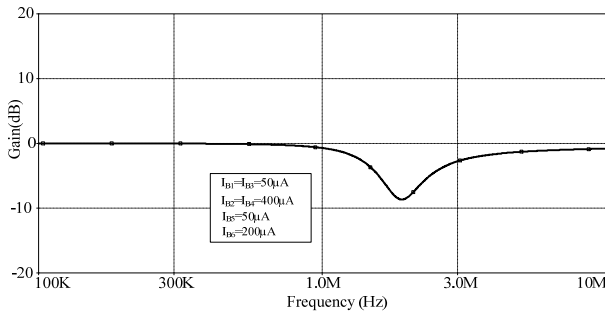


Figure 8. Gain response of the I_{B5} .

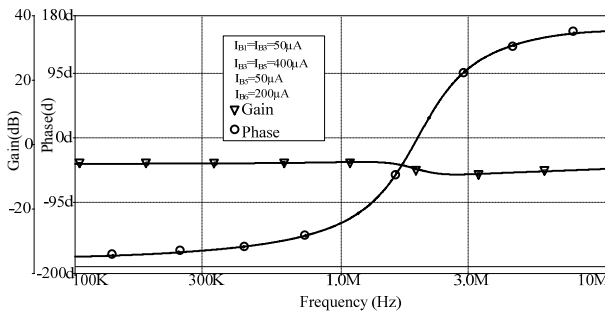


Figure 9. Gain and phase responses of the I_{AP} .

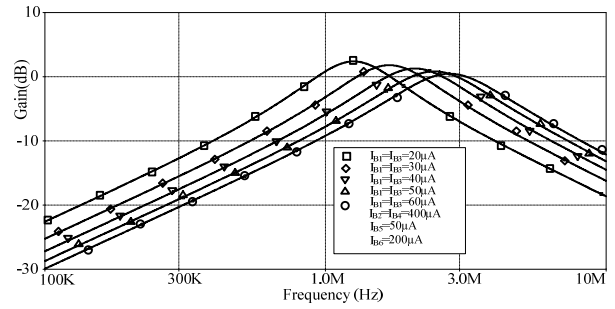


Figure 10. Band-pass responses for different values of I_{B1} and I_{B3} with keeping its ratio to be constant as $I_{B1} = I_{B3} = I_B$.

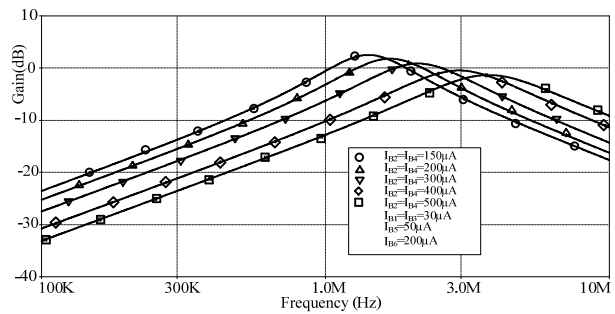


Figure 11. Band-pass responses for different values of I_{B2} and I_{B4} with keeping its ratio to be constant as $I_{B2} = I_{B4} = I_B$.

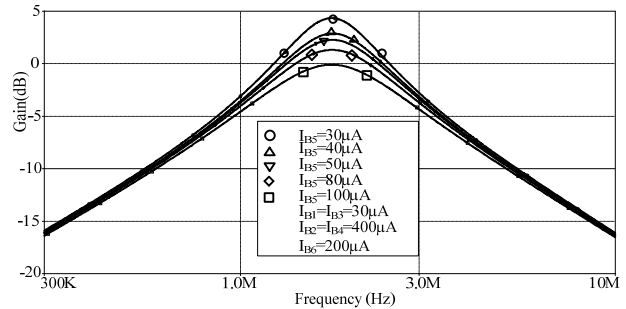


Figure 12. Band-pass responses for different values of I_{B5} .

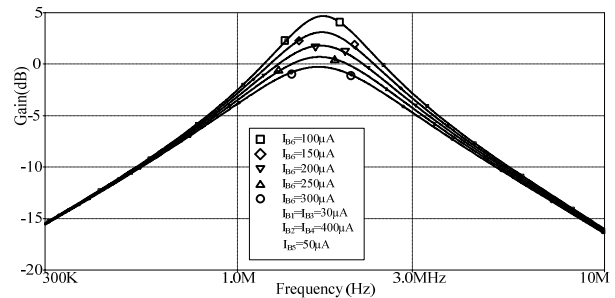


Figure 13. Band-pass responses for different values of I_{B6} .

sponses of the band-pass function relative to different temperatures of 27°C, 50°C and 100°C. From this result, it can be measured that the average deviation of parameters ω_0 and Q_0 due to these temperature variations is

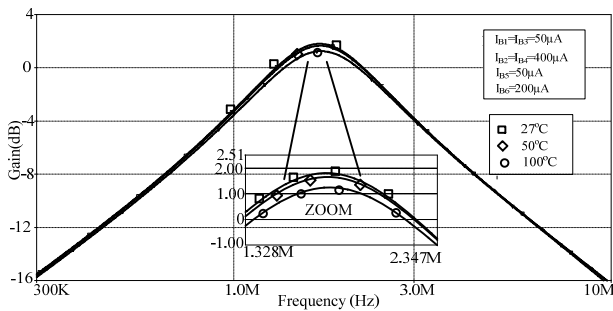


Figure 14. Band-pass responses for different temperature values.

merely 0.005196829%/°C. Consequently, it is concluded that the pole frequency and quality factor are slightly dependent on the temperature variations.

6. Conclusion

The current-mode biquad filter based on the differentiator configuration consisting of 3 CCCCTAs and 2 internally frequency compensated operational amplifiers has been presented. The advantages of the proposed circuit are that: it can simultaneously perform 3 standard functions, low-pass high-pass and band-pass functions from the same circuit configuration, where the rest functions (band-stop and all-pass) can be easily provided by small modification without component matching constraint and changing circuit topology; the pole frequency can be electronically tuned without affecting the quality factor with keeping their ratios constant. In addition, the pole frequency and quality factor are slightly temperature dependent. The PSpice simulation results were depicted, and agreed well with the theoretical anticipation. With mentioned features, it is very suitable to realize the proposed circuit in monolithic chip to use in battery-powered, portable electronic equipments such as wireless communication system devices.

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