

Table of Contents

Volume 7 Number 5

April 2016

Jitter Reduced Self Biased PLLs—A Systematic Simulation Study	
J. D. Devi.....	533
Multifunction Filter Employing Current Differencing Buffered Amplifier	
T. S. Arora, U. Rana.....	543
Architectural Design of 32 Bit Polar Encoder	
G. Indumathi, V. P. M. B. A. A. Ananthakirupa, M. Ramesh.....	551
Optimization of Thermal Aware VLSI Non-Slicing Floorplanning Using Hybrid Particle Swarm Optimization Algorithm-Harmony Search Algorithm	
S. Paramasivam, S. Athappan, E. D. Natrajan, M. Shanmugam.....	562
Closed Loop Control of Bi-Directional Soft Switched Quasi Z-Source DC-DC Converter	
A. Suresh, M. R. Rashmi, V. Madusuthanan, P. V. Kumar.....	574
A New Front end Capacitive Converter Fed Switched Reluctance Motor for Torque Ripple Minimization	
S. Muthulakshmi, R. Dhanasekaran.....	585