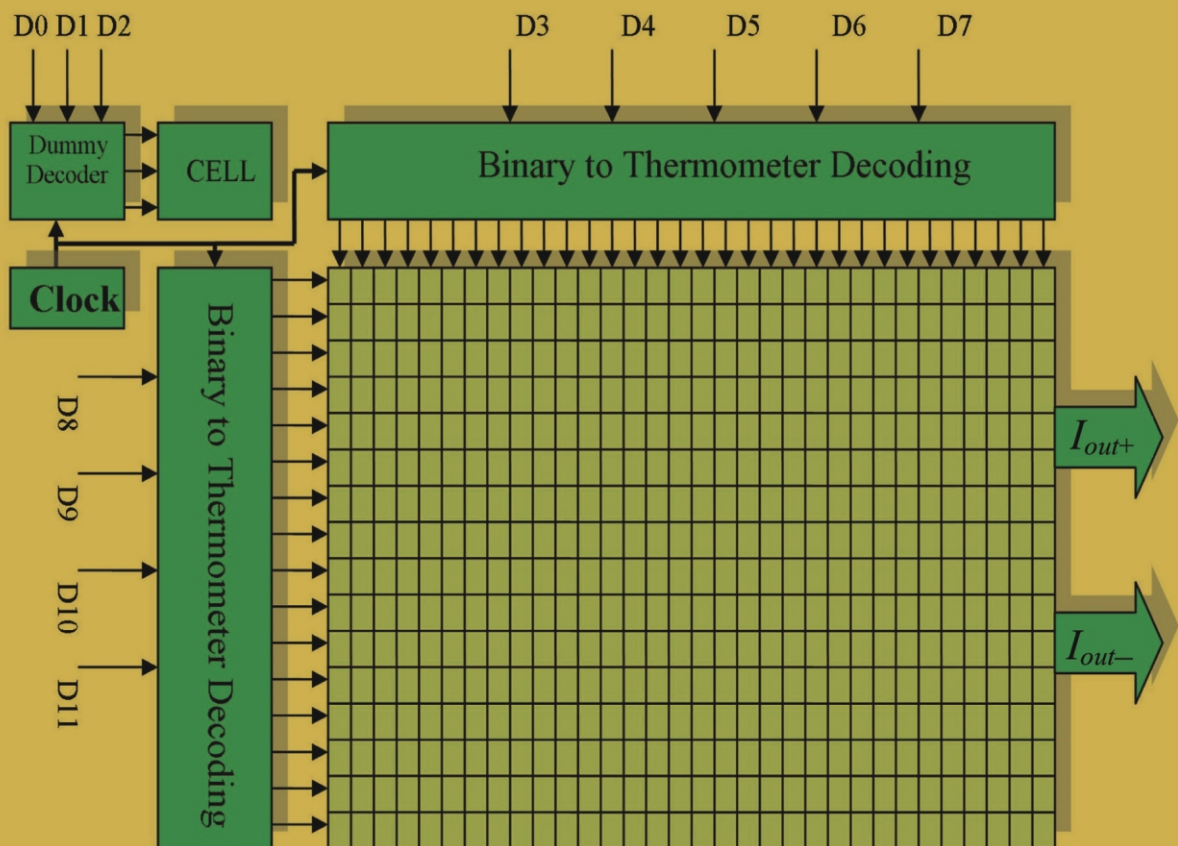




Circuits and Systems



Block diagram of a conventional row and column decoded 12-bit current-steering DAC.



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