

# Total Harmonic Distortion Minimization of Multilevel Converters Using Genetic Algorithms

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#### **ABSTRACT**

This paper presents a genetic algorithm (GA) optimization technique to find the optimum switching angles of 11-level inverter with minimum number of dc sources and switches in comparison with the cascade multilevel inverter in order to minimize the total harmonic distortion (THD) of their output voltage waveform. Theoretical and simulation results for an 11-level converter show the efficiency of the proposed algorithm to determine the optimum angles in order to decrease the undesired harmonics and produce very high quality output voltage waveform.

Keywords: Genetic Algorithm; Multilevel Converter Topologies; Multilevel Inverter

#### 1. Introduction

Multilevel inverters have become an effective and practical solution for increasing the power and reducing the harmonics of AC waveforms. Compared to the traditional two-level voltage source inverters, the stepwise output voltage is the major advantage of multilevel inverters. This advantage results in higher power quality, better electromagnetic compatibility, lower switching losses, higher voltage capability, and needlessness of a transformer at distribution voltage level, thereby reducing the costs [1].

Among The topologies for high power multilevel inverters, the cascaded inverter has the advantages that the DC-link voltage is balanced, the modularized circuit layout is possible, and it has the least components per phase. As the number of levels increases, multilevel inverters have low harmonic components and a low switching frequency, leading to the advent of step-pulse waves that switch once per fundamental cycle. Individual devices operate at high efficiencies because they can switch at a much lower frequency than PWM-controlled inverters [2]. For output step-pulse waveforms, it is necessary to obtain the conducting angles of switching devices. The predominant low-order harmonics can be eliminated. However, it has to solve conventional method has the merit that the simultaneous equations, which are the set of nonlinear transcendental equations for the fundamental component and the harmonic ones. It is difficult to obtain the conducting angles because the conventional

method needs an iterative method such as the Newton-Raphson one. Additionally, the switching angles are obtained by means of an off-line calculation to minimize the harmonics for each modulation index, which leads to increased use of look-up tables.

The THD of the output voltage of the inverter is a measurement of the harmonic distortion, which is expected to be as small as possible in many applications of multilevel inverters. One popular example is the single-phase multilevel inverter for the photovoltaic power supply system, in which the THD of the output voltage of the inverter is regarded as a very important measurement of voltage quality [3].

This paper proposes a general genetic algorithm (GA) approach by which the switching angles can be calculated in the context of step modulation for multilevel inverters. This solves the same problem with a simpler formulation and with any number of levels without extensive derivation of analytical expressions. In this paper, the optimization aim of the proposed algorithm is the minimization of the voltage THD. Comparison with other methods also shows that the THD gained by the proposed method is the smallest. Simulation results are given to verify the performance of the proposed method.

## 2. The Circuit Topology

**Figure 1** shows the circuit topology of the 11-level inverter. dc voltage sources are independent each other. Switches  $T_a$ ,  $T_b$  and diode  $D_a$  are the switches and the

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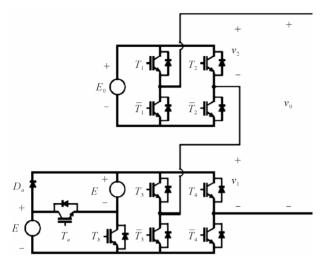


Figure 1. 11 Level inverter.

diode which switch the dc voltage sources in series and in parallel. When the switch  $T_a$  becomes off and the switch  $T_b$  becomes on, the current flows in the switch  $T_b$  and the diode  $D_a$ , which connect the E voltage sources in parallel. Then, when the switch  $T_a$  becomes on and the switches  $T_b$  becomes off, the current flows in the switch  $T_a$ , which connect the E voltage sources in series via the switch  $T_a$ . Using this conversion of dc voltages, outputs 11 levels by  $v_1 + v_2$  or  $v_1 - v_2$  [4].

# 3. Multilevel Inverter with Step Modulation

Various topologies, such as diode clamped multilevel inverters and cascade multilevel inverters, have been applied in industries. An important family of modulation methods for multilevel inverters is the optimal PWM. A representative of this family is step modulation.

Take the example of the 11-level inverter shown in **Figure 1**, where the basic operation principle of the multilevel inverter and the step modulation strategy is explained. **Figure 2** shows the output voltage waveforms of an 11-level inverter under step modulation.  $\theta_1, \theta_2, \dots, \theta_5$ , are switching angles that indicate the on or off instant of switches in **Figure 1** structure.

By applying Fourier series analysis for a cascade multilevel inverter with s switching angles, the amplitude of any odd nth harmonic of output voltage can be expressed as:

$$V_n = \frac{4}{n\pi} \times \sum_{k=1}^{s} \left[ V_{dc} \cos(n\alpha_k) \right]$$
 (1)

where n is an odd harmonic order and  $\theta_k$  is the kth switching angle. The amplitudes of all even harmonics are zero. The modulation index m is defined as:

$$m = \frac{\pi}{4} \frac{V_1}{V_{\text{max}}} = \frac{\pi}{4} \frac{V_1}{s \cdot V_{dc}}$$
 (2)

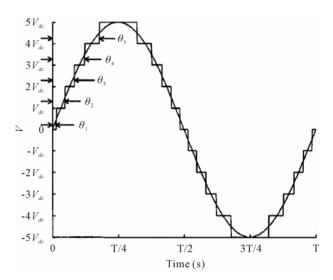


Figure 2. Output voltage of the multilevel inverter and its switching angles  $\left(E_{_0}=V_{_{dc}}=\frac{E}{2}\right)$ .

 $V_{\rm max}$  is the maximum attainable output voltage of the inverter. In the case of the 11-level inverter, s is five and  $V_{\rm max}$  is 5  $V_{dc}$ .

# 4. Optimization Aim

There are different optimization aims for different applications: 1) minimization of the THD of the output voltage of the inverter, for many applications such as photovoltaic systems [3]; 2) elimination of lower order odd harmonics of the load voltage, such as the third, fifth, and seventh..., for applications equipped with appropriate filters; 3) minimization of the THD but ignoring triple order harmonics that can be cancelled in specific three-phase systems [5]; and 4) elimination of lower order odd harmonics but ignoring the triple order harmonics. This paper focuses on the first aim: minimization of the voltage THD. The amplitude of any odd nth harmonic of the output voltage is shown in (3). Thus, the voltage THD is expressed as:

THD = 
$$\sqrt{\frac{\sum_{n=3,5,7,...} V_n^2}{V_1^2}}$$
 (3)

It seems difficult to achieve the minimization of THD directly because the numerator of (3) has infinitely many terms. One solution may be to eliminate only a finite number of harmonics. Generally, one eliminates a few lower order harmonics because they seem to contribute more to THD. In a cascade multilevel inverter with s HBs, only (s-1) lower order harmonics could be eliminated. Take the example of an 11-level inverter, the third; fifth, seventh and ninth harmonics can be eliminated as shown in:

$$\cos(\theta_1) + \cos(\theta_2) + \cos(\theta_3) + \cos(\theta_4) + \cos(\theta_5) = 5m_1$$

$$\cos(3\theta_1) + \cos(3\theta_2) + \cos(3\theta_3) + \cos(3\theta_4) + \cos(3\theta_5) = 0$$

$$\cos(5\theta_1) + \cos(5\theta_2) + \cos(5\theta_3) + \cos(5\theta_4) + \cos(5\theta_5) = 0$$

$$\cos(7\theta_1) + \cos(7\theta_2) + \cos(7\theta_3) + \cos(7\theta_4) + \cos(7\theta_5) = 0$$

$$\cos(9\theta_1) + \cos(9\theta_2) + \cos(9\theta_3) + \cos(9\theta_4) + \cos(9\theta_5) = 0$$
(4)

several methods, such as Newton-Raphson iteration method [3], methods based on symmetric polynomials and resultants [3], and methods based genetic algorithms [6], have been proposed to solve the nonlinear transcendental equation set as show in (4). In a cascade multilevel inverter with s HBs in the context of step modulation, if more than (s-1) harmonics are considered to minimize THD, these harmonics cannot be eliminated completely since the number of variables is more than the number of equations in the equation set. Minimization of THD in this context has not been investigated until now.

# 5. Genetic Algorithm (GA)

The aim is to determine the optimum switching angles that generate an output voltage with the required fundamental component and the possible minimum THD. This is a problem to be solved by an optimization algorithm. In this paper, GA is used which is a simple, powerful, and evolutionary technique, inspired from the laws of natural selection and genetics. It is a general-purpose stochastic global search algorithm, with no need of functional derivative information to search for the solutions that minimize (or maximize) a given objective function. GA reduces the computational burden and search time, while solving complex objective functions [5]. To minimizes a function  $f(x_1, x_2, \dots, x_k)$  using GA, first, each  $x_i$  is coded as a binary or floating-point string of length. In this letter, a binary string is preferred, e.g.

$$x_1 = \begin{bmatrix} 10001 & \cdots & 01001 \end{bmatrix}$$

$$x_2 = \begin{bmatrix} 00101 & \cdots & 11110 \end{bmatrix}$$

$$\vdots$$

$$x_k = \begin{bmatrix} 11110 & \cdots & 01011 \end{bmatrix}$$

The set  $\{x_1, x_2, \dots, x_k\}$  is called a chromosome and  $x_i$  are genes.

## 6. Formulation of Problem

The GA methodology is the same for any application. There are only a few parameters to be set for a GA to work. The steps for formulating a problem and applying a GA are as follows:

- 1) Select binary or floating point strings;
- 2) Find of the variables specific to the problem; this number will be the number of genes in a chromosome. In

this application, the number of variables is the number of controllable switching angles. Each chromosome for eleven-level inverter will have five switching angles, *i.e.*  $\{\theta_1, \theta_2, \theta_3, \theta_4, \theta_5\}$ ;

- 3) Set a population size and initialize the population. Higher population might increase the rate of convergence, but it also increases the execution time. The selection of an optimum-sized population requires some experience in GA. The population in this paper has 20 chromosomes, each containing five switching angles. The population is initialized with random angles between 0 and 90 taking into consideration the quarter-wave symmetry of the output voltage waveform;
- 4) The most important item for the GA to evaluate the fitness of each chromosome is the cost function. The objective of this study is to minimize total harmonic distortion of output voltage. Therefore, the cost function f is expressed as:

$$f(\theta_1, \theta_2, \theta_3, \theta_4, \theta_5) = \sqrt{\frac{\sum_{n=3,5,7,...} V_n^2}{V_1^2}}$$
 (5)

where  $\theta_i$  are the switching angles and  $V_n$  are the *n*-th order voltage harmonics. For each chromosome, a multilevel output voltage waveform is created using the switching angles in the chromosome. Typically, the GA algorithm is formulated as a maximization problem rather than a minimization problem. In cases where minimization is required, the negative or the reciprocal of the function to be optimized is used. Using this formulation, the fitness value FV is calculated for each chromosome using

$$fv(\theta_1, \theta_2, \theta_3, \theta_4, \theta_5) = -\sqrt{\frac{\sum_{n=3,5,7,...} V_n^2}{V_1^2}}$$
 (6)

The switching angle set producing the maximum FV is the best solution of the first iteration;

5) The GA is usually set to run for a certain number of iterations (100 in this case) to find an answer. After the first iteration, FV's are used to determine new offspring. These go through crossover and mutation operations and a new population is created which goes through the same cycle starting from FV evaluation. Sometimes, the GA can converge to a solution well before 100 iterations are completed. The solution must also satisfy the following basic constraint:

$$\theta_1 < \theta_2 < \theta_3 < \theta_4 < \theta_5 < \pi/2$$

## 7. Optimization Aim

To demonstrate the validity of the proposed method, a simulation study was carried out using MATLAB/SIMULINK. GA is employed to minimize the objective function for the whole range of modulation index. The obtained solutions for optimum switching angles  $\theta_1, \theta_2, \cdots, \theta_5$  are plotted in **Figures 3-6** that shows the optimum switching angles when this technique is applied to an 11-level inverter to minimize the THD. For a three-phase load, the line-to-line voltage THD is important, since the triplen harmonics, present in the phase voltage, are eliminated from the line voltage. For the switching angles obtained by Newton-Raphson approach the phase-voltage and line-voltage THD is computed and plotted in **Figures 3** and **4** respectively.

With genetic algorithm approach the line-voltage THD and switching angles is computed and plotted versus modulation index in **Figures 5** and **6** respectively.

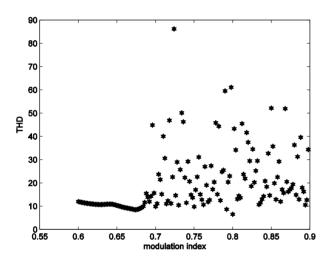


Figure 3. THD versus modulation index of for 11-level 1 phase inverter with Newton-Raphson.

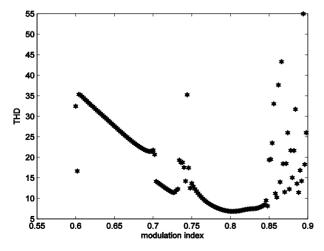


Figure 4. THD versus modulation index of for 11-level 3 phase inverter with Newton-Raphson.

This results show that genetic algorithm is optimized the THD in comparison with Newton-Raphson. The results of two approaches are shown in **Table 1**.

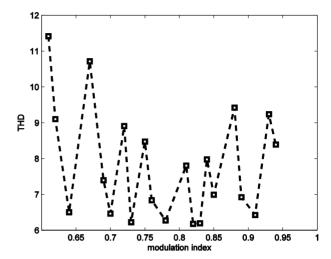


Figure 5. THD versus modulation index of for 11-level inverter with genetic algorithm.

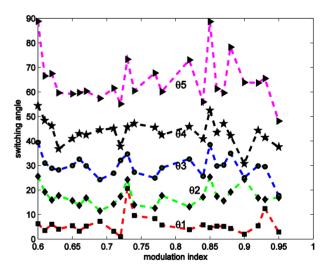


Figure 6. Optimum switching angles versus modulation index of for 11-level inverter.

Table 1. Comparison of 2 approaches.

Optimum switching angles & THD for 3 phase 11-level inverter		
Optimum switching angles & THD	Newton-Raphson	Genetic algorithm
$\theta$ 1	6.39	5.48
$\theta$ 2	18.9	16.8
$\theta$ 3	26.8	28.98
$\theta$ 4	44.78	42.1
$\theta$ 5	62.08	60.7
THD	6.83	6.08#

## 8. Conclusion

This paper has proposed a novel algorithm by which switching angles can be obtained in the context of step modulation for multilevel inverters. With the algorithm, the voltage THD is minimized, which is verified by the THD comparison. The proposed method is able to find the optimum switching angles in a simple manner. The simulation results are provided for an 11-level inverter to validate the accuracy of the computational results.

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