

New Single Input Multiple Output Type Current Mode Biquad Filter Using OTAs

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Abstract

This paper presents a new current mode (CM) single-input and multi-output (SIMO)-type biquad using two multiple output OTAs and one current follower as an active device and having two grounded capacitors. This SIMO type circuit realizes all the five filter functions as low pass, band pass, high pass, band reject and all pass filter transfer functions simultaneously. This circuit has the unity gain transfer function for all the five types of filters. The circuit enjoys electronic tunability of angular frequency and bandwidth. The 0.18 μ m TSMC technology process parameters have been utilized to test and verify the performance characteristics of the circuit using PSPICE. The sensitivity analysis, transient response and calculations of total harmonic distortion have also been shown.

Keywords

Active Filters, Current Mode, Operational Transconductance Amplifier, Universal Filter

1. Introduction

OTA-C structures are suitable for realizing electronically-tunable continuous-time filters in a variety of technologies such as bipolar, CMOS and Bi-CMOS and therefore, widely used for designing voltage mode (VM) as well as current-mode (CM) filters. Although a number of CM OTA-C biquads are reported in earlier literature [1]-[22], those of [1]-[6] are of multiple-input-multiple-output type or multiple-input-single-output type [7]-[9]. Thus, only the circuits of [10]-[22] realize single-input-multiple-output (SIMO) type CM biquad filters, with which this paper is concerned. SIMO type biquad using five active devices and two grounded capacitors (GCs) is given in [15] [18] [20], that by using four active devices and two GCs is given in [12]-[14] [16] [17] [22]. In [10], SIMO type biquad is made by using three active devices, one grounded and one floating capacitors presented whereas in [19] [21] SIMO type biquad made by using three active devices, two GCs and one additional passive element. In [25] SIMO type biquad presented using two active devices and two grounded capacitors, in this circuit input current is not at low impedance terminal. In the next section proposing a new SIMO type CM mode biquad using three active devices and two GCs with following desirables features: 1) realizibility of all the five standard filter functions namely, low pass (LP), band pass (BP), high pass (HP), band reject (BR) and all pass (AP); 2) realizibility of all the five functions without requiring any design constraint/matching conditions; 3) availability of explicit current outputs at high-output-impedance nodes and the input at low input impedance node; 4) independent tunability of ω_0 and bandwidth (BW) with the gain is unity (v) employment of both grounded capacitors; and 5) use of a small number of active building blocks.

2. Proposed Circuit

The Operational Transconductance Amplifier (OTA) is the widely used and most significance active building block. This happens to be an attractive active device because it offers electronic tunability of filter parameters. So the circuit diagram of the Operational Transconductance Amplifier (OTA) is **Figure 2(a)**. The characteristic equations of the OTA are given as (1)-(2) and its MOS implementation is shown in **Figure 2(b)** as shown in [24].

$$\begin{bmatrix} I_{y} \\ V_{x} \\ I_{z} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & \pm 1 & 0 \end{bmatrix} \begin{bmatrix} V_{y} \\ I_{x} \\ V_{z} \end{bmatrix}$$
(1)

$$i_0 = g_{m1} \left(V_1 - V_2 \right) \tag{2}$$

The proposed new circuit configuration is shown in Figure 1.

A straight forward analysis reveals the following transfer functions for the configuration of Figure 1

LPF:
$$\frac{i_{LP}}{i_{in}} = \frac{1}{D(s)} [g_{m1}g_{m2}]$$
 (3)

BPF:
$$\frac{i_{BP}}{i_{in}} = \frac{-1}{D(s)} \left[sC_2 g_{m1} \right]$$
(4)

BRF:
$$\frac{i_{BR}}{i_{in}} = \frac{i_{in} + i_{BP}}{i_{in}} = \frac{1}{D(s)} \left[s^2 C_1 C_2 + g_{m1} g_{m2} \right]$$
 (5)

HPF:
$$\frac{i_{HP}}{i_{in}} = \frac{i_{in} + i_{BP} - i_{LP}}{i_{in}} = \frac{1}{D(s)} \Big[s^2 C_1 C_2 \Big]$$
 (6)

APF:
$$\frac{i_{AP}}{i_{in}} = \frac{i_{BP} + i_{BR}}{i_{in}} = \frac{1}{D(s)} \Big[s^2 C_1 C_2 - s C_2 g_{m1} + g_{m1} g_{m2} \Big]$$
 (7)



Figure 1. The proposed configuration.

where, $D(s) = s^2 C_1 C_2 + s C_2 g_{m1} + g_{m1} g_{m2}$.

The various parameters of the realized filters are given by:

$$\omega_0 = \sqrt{\frac{g_{m1}g_{m2}}{C_1 C_2}}, \quad BW = \frac{g_{m1}}{C_1} \quad \text{and} \quad Q_0 = \sqrt{\frac{C_1 g_{m2}}{g_{m1} C_2}}$$
(8)

Note that from Equation (8) it is clear that the bandwidth can be fixed by g_{m1} and then center frequency can be controlled by g_{m2} .

Sensitivity analysis for this confirmation shows that

$$S_{g_{m1}}^{\omega_0} = S_{g_{m2}}^{\omega_0} = -S_{C_1}^{\omega_0} = -S_{g_{m1}}^{\omega_0} = \frac{1}{2}, \quad S_{g_{m1}}^{\mathcal{Q}_0} = S_{g_{m2}}^{\mathcal{Q}_0} = -S_{C_1}^{\mathcal{Q}_0} = S_{C_2}^{\mathcal{Q}_0} = -\frac{1}{2}$$
(9)

which all are no more than 0.5.

3. Simulation Results

The workability of the proposed new circuits has been verified by SPICE simulations using CMOS OTAs is shown in **Figure 2** (as shown in [24]) and CMOS current followers is shown in **Figure 3** (as shown in [23]). Whereas aspect ratios for various MOSFETs employed in the differential-input-dual-output (DIDO)-type OTAs is shown in **Table 1**, the supply voltages used were $V_{DD} = 1.0$ V, $V_{SS} = 1.0$ V, and the aspect ratio for the CMOS current follower is shown in **Table 2**, and the supply voltages used were $V_{DD} = 1.0$ V, $V_{SS} = 1.0$ V, $V_{SS} = 1.0$ V, $V_{SIAS1} = -0.47$ V, $V_{BIAS2} = -0.71$ V, whereas 0.18 µm CMOS technology parameters obtained from TSMC were used.



Figure 2. (a) Symbolic notation of MO-OTA; (b) CMOS realization of the MO-OTA [24].



Figure 3. An exemplary CMOS implementation of the dual output current follower (DOCF) [23].

Table 1. Aspect ratios of MO-OTA.		
MOSFET	W (μm)	L (µm)
M1, M2	5.76	0.72
M3, M4, M5, M6, M7, M8	2.16	0.72
M9, M10, M11, M12	1.44	0.72
Table 2. Aspect Ratios of dual output c	urrent follower (DOC	F).
MOSFETS	W (μm)	L (µm)
		0 12

M1-M4	5.714	0.43
M5-M8, M13-M16, M21-M24, M27-M28	9.285	0.43
M9-M12, M17-M20, M25-M26	18.571	0.43

To achieve the SIMO-type filters with $f_0 = 1$ MHz and quality factor of $Q_0 = 0.73$, the capacitors values were selected $C_1 = C_2 = 9$ pF and the trans-conductance (g_m) parameters were $g_{m1} = 80.6 \mu$ A/V and $g_{m2} = 39.2 \mu$ A/V. All the five filter responses are shown in **Figure 4**. Continuous line in **Figure 4**(a) shows the simulated outputs whereas dashed line represents the ideal or theoretical output of the derived filter configuration. For AP response, the magnitude as well as phase response is shown in **Figure 4**(b). One can see in **Figure 4**(a) the simulated and ideal responses are in good agreement to each other.

To test the input dynamic range of the proposed filters, the simulation of the BPF as an example has been done for a sinusoidal input signal of $f_0 = 1$ MHz. Figure 5 shows that the input dynamic range of the filter extends up to amplitude of 5 μ A. The dependence of the output harmonics distortion on the input signal amplitude is illustrated in Figure 6.

For input signal amplitude lower than $\$\mu$ A the total harmonic distortion (THD) has been found to be of less than 4.0%. The obtained results show that the circuit operates properly even at signal amplitudes of about $\$\mu$ A. **Figure 7** shows the simulation results for variation of Q_0 while keeping f_0 fixed (1 MHz) with $C_1 = C_2 = 9$ pF (see **Table 3**). **Figure 8** shows the simulation results for variation f_0 of while keeping $Q_0 = 1$ with $C_1 = C_2 = 9$ pF (see **Table 3**).



Figure 4. PSPICE Simulation results: (a) Gain response of LPF, BPF, HPF and Notch; (b) Gain and Phase response of APF.



Figure 5. Time domain response of the band-pass filter of the proposed circuit for 1 MHz sinusoidal input current of $5 \,\mu$ A.



Figure 6. Dependence of output current total harmonic distortion on input current amplitude of the band-pass filter of proposed configuration.



Figure 7. Simulation results for control of Q_0 while keeping f_0 fixed (1 MHz) for band pass filter.



Figure 8. Simulation results for control of f_0 while keeping Q_0 (=1) fixed for band pass filter.

Fixed f_0				Fixed Q_0		
Q_0	g_{m1} (μ A/V)	g_{m2} (μ A/V)	$f_0{ m MHz}$	g_{m1} (µA/V)	g_{m2} (μ A/V)	
1	55.5	55.5	1	55.5	55.5	
2	27.6	111.3	15.8	89.8	89.8	
3	18.5	166.3	20.41	115.4	115.4	

Table 3. The g_{m1} and g_{m2} values for controlling of Q_0 and for controlling f_0 .

4. Conclusion

This paper introduces a new universal filter which employs one current follower, two OTAs and only two grounded capacitors. This circuit has been verified theoretically and simulated using Orcad PSPICE software. The circuit consists of the following features: 1) all the five standard filtering responses are available without putting any additional active or passive component/device; 2) the current mode circuit offers electronic tunability for the frequency as well as for the bandwidth; 3) the input is applied at low impedance port and the output is taken from high impedance port, and makes this circuit a better proposition for utilizing this circuit for making the higher order filter; 4) all the used capacitors are grounded in nature, which is an important parameter for

integrated circuit realization; 5) all the current outputs are explicitly available; 6) this SIMO type universal filter offers unity gain. All the simulated results are in agreement with the theoretical results.

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